

## Table of Contents

Paperboy™ Main Wiring Diagram.....	Sheet 1B
Paperboy Regulator/Audio III PCB Schematic Diagram .....	Sheet 2A
Paperboy Video Display Wiring Diagram .....	Sheet 2B
Paperboy Control Panel Wiring Diagram .....	Sheet 3A
Paperboy Game Interfaces.....	Sheet 3B
Fluorescent Light Wiring Diagram	
Coin Door Wiring Diagram	
Utility Panel Wiring Diagram	
Paperboy Main Microprocessor (T-11) Memory Map .....	Sheet 4A
Paperboy Sound Microprocessor (6502) Memory Map .....	Sheet 4B
<b>Paperboy CPU PCB Schematic Diagrams:</b>	
Power Input .....	Sheet 5A
Extender Socket, Mode Register, T-11 Clock, Clock Stretching, Interrupt Logic, T-11 Microprocessor and Address Latches .....	Sheet 5B
Address Decoders, 6502 Microprocessor Communication Flags.....	Sheet 6A
Paged Program ROM, Reset and Watchdog Clear .....	Sheet 6B
6502 Microprocessor Communication Latches, Fixed Program ROM, Zero-Page RAM, Control Panel Inputs .....	Sheet 7A
CPU/Video Buffers, Clock and Control Signals .....	Sheet 7B
6502 Microprocessor, Program Memory, Zero Page RAM and EEROM, Address Decoding .....	Sheet 8A
A/D Converter, Address Decoding, Coin Counters, LED Drivers .....	Sheet 8B
<b>Paperboy Video PCB Schematic Diagrams:</b>	
Coin Door and Status Inputs, Speech, Music, Power Connections.....	Sheet 9A
Audio Output Drivers, Sound Effects Option Switches, Trak Ball Control.....	Sheet 9B

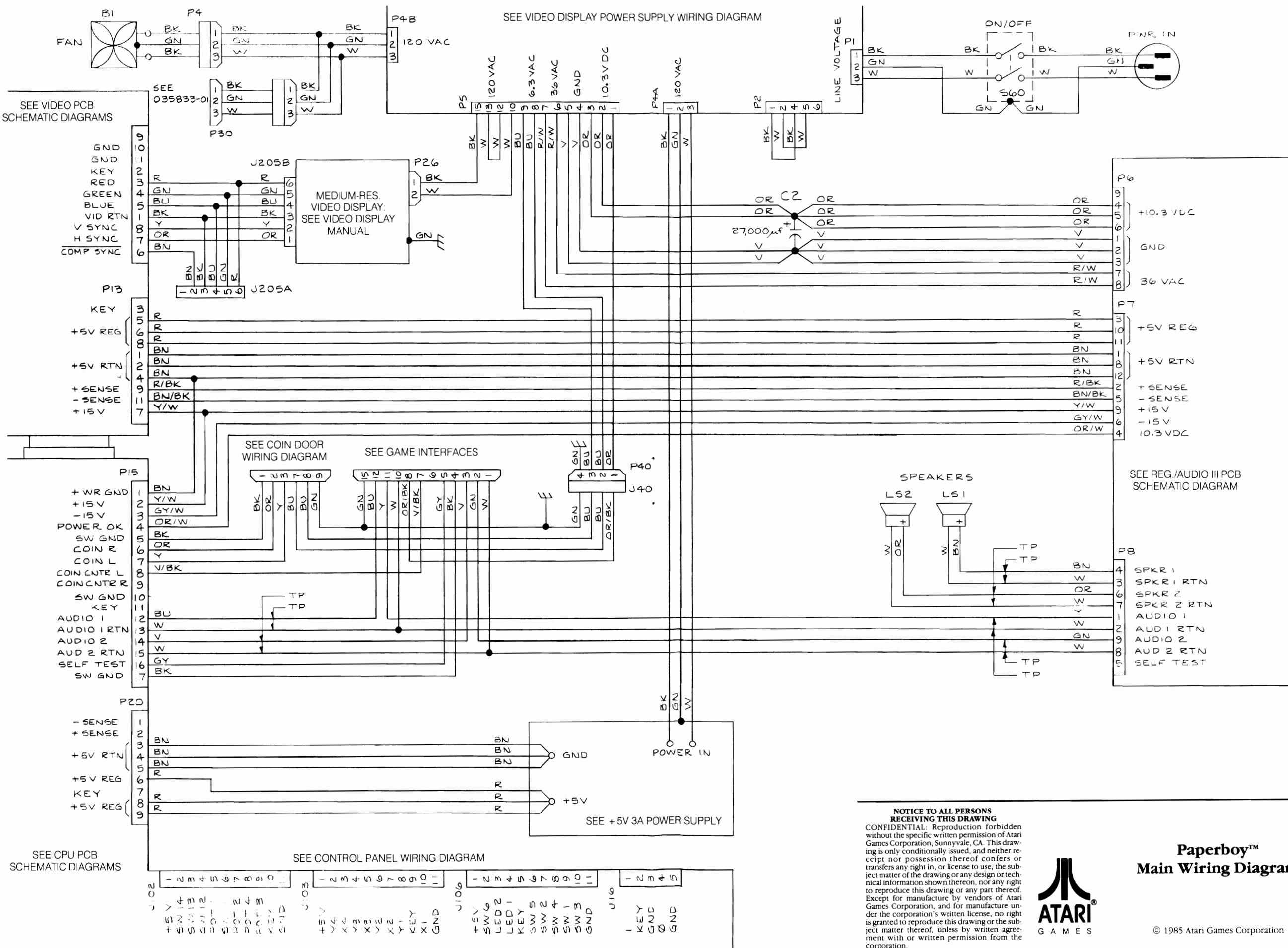
## Schematic Package Supplement to



**NOTE**  
This staple temporarily holds the schematic package together. Remove the staple before using these schematics.

## Operators Manual



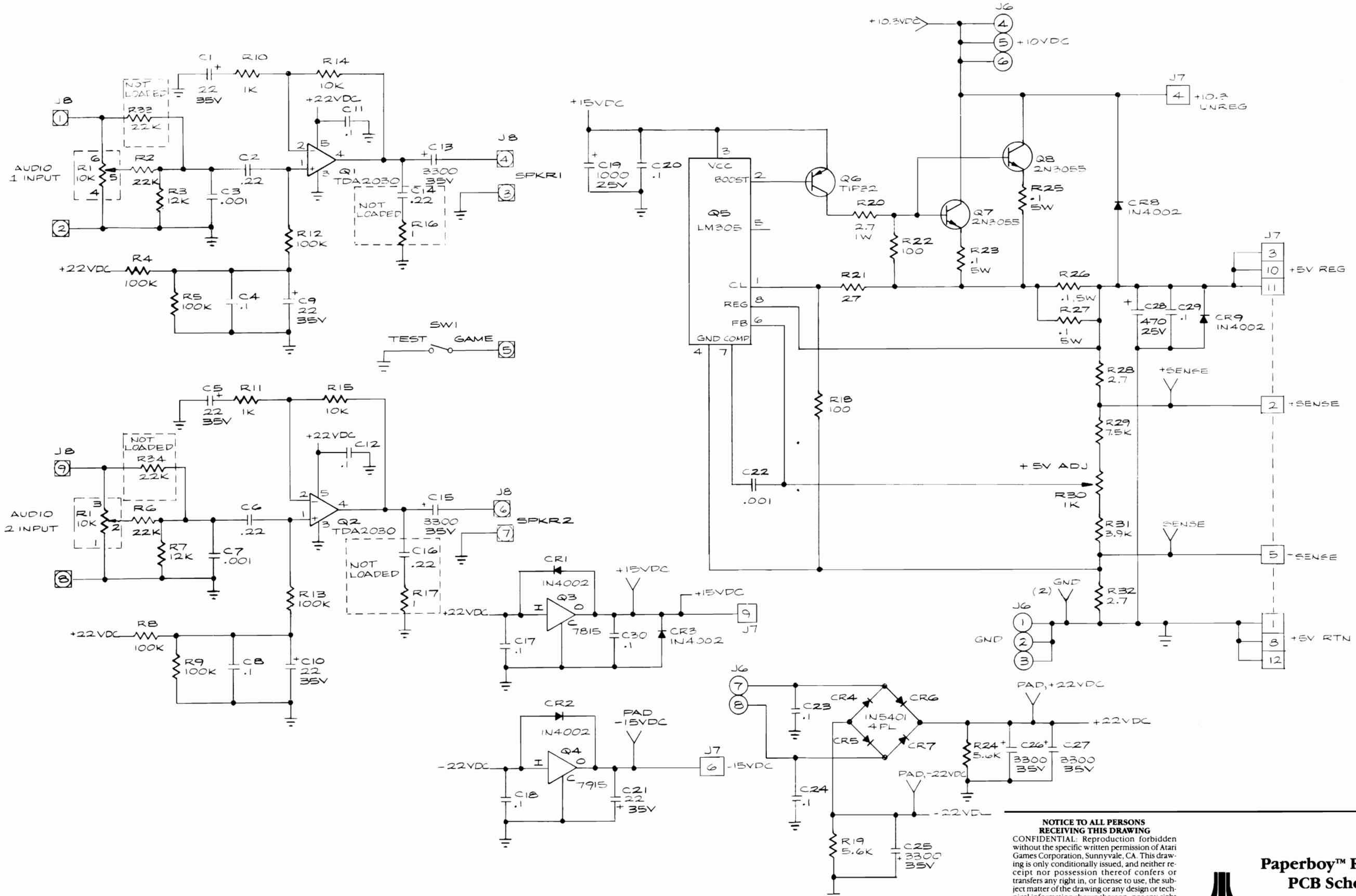


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**Paperboy™  
Main Wiring Diagram**

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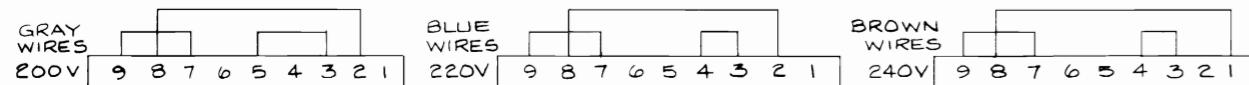


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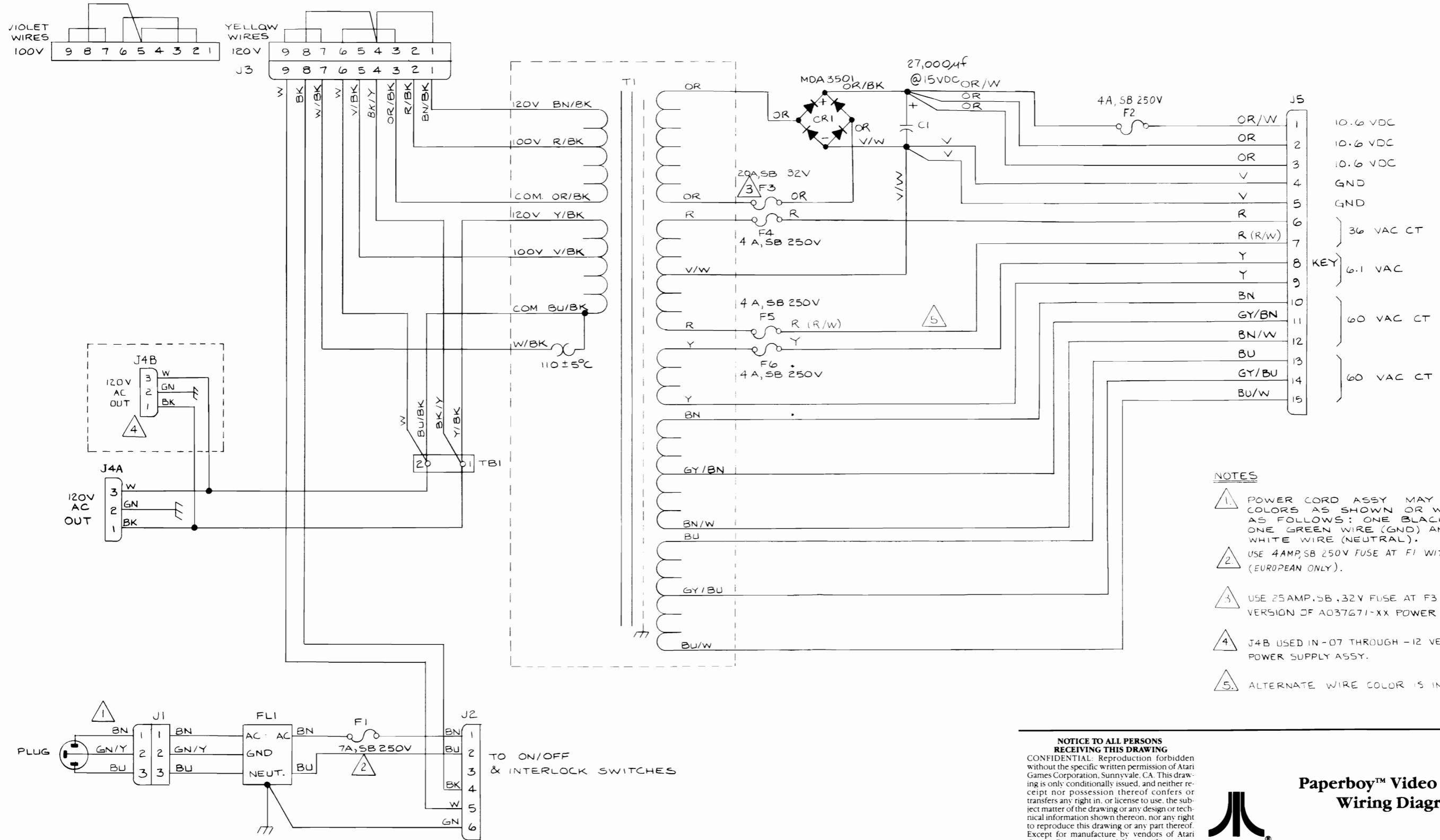
**Paperboy™ Regulator/Audio III  
PCB Schematic Diagram**



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## VOLTAGE SELECTION BLOCKS

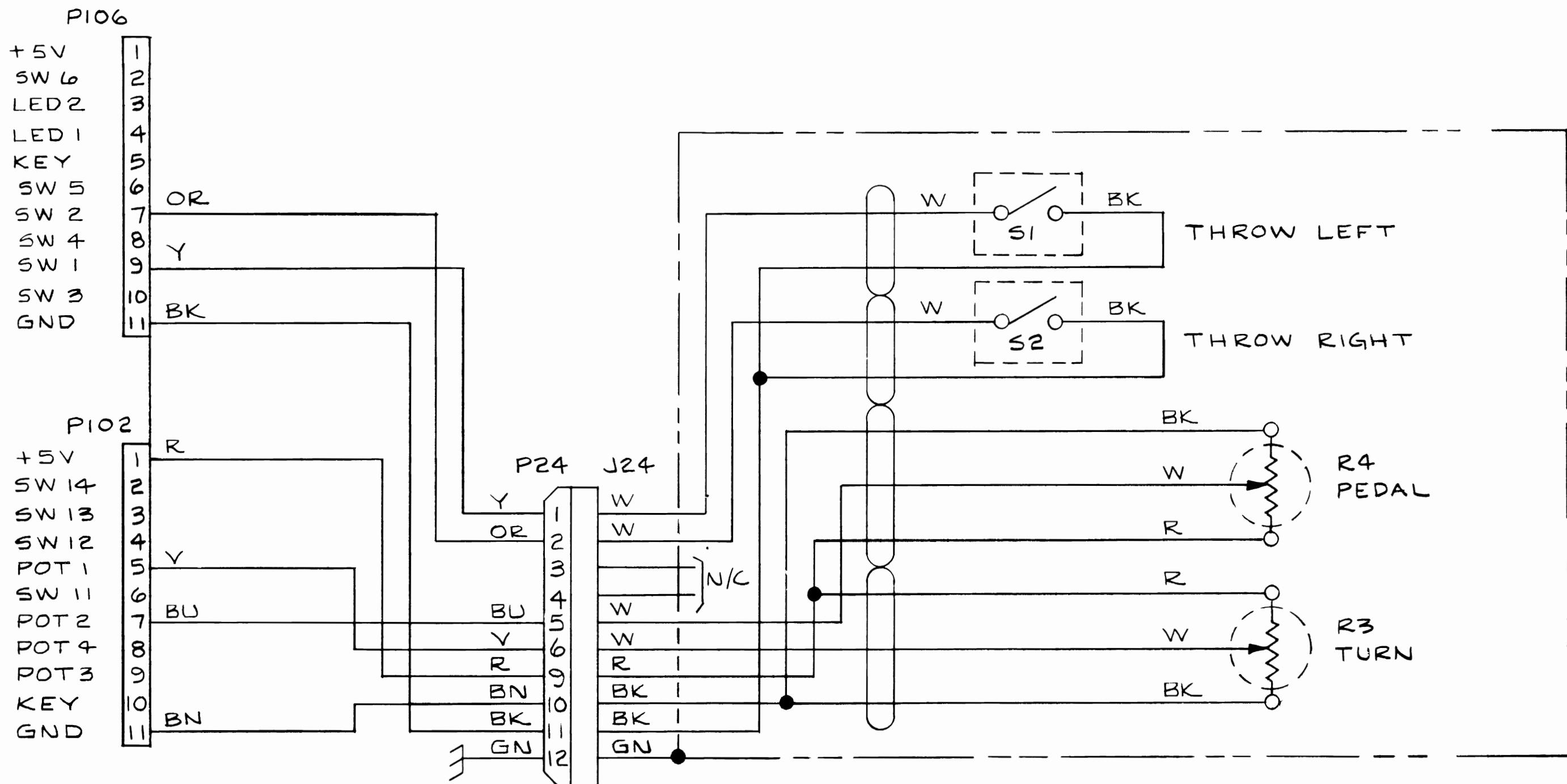


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## **Paperboy™ Video Display Wiring Diagram**



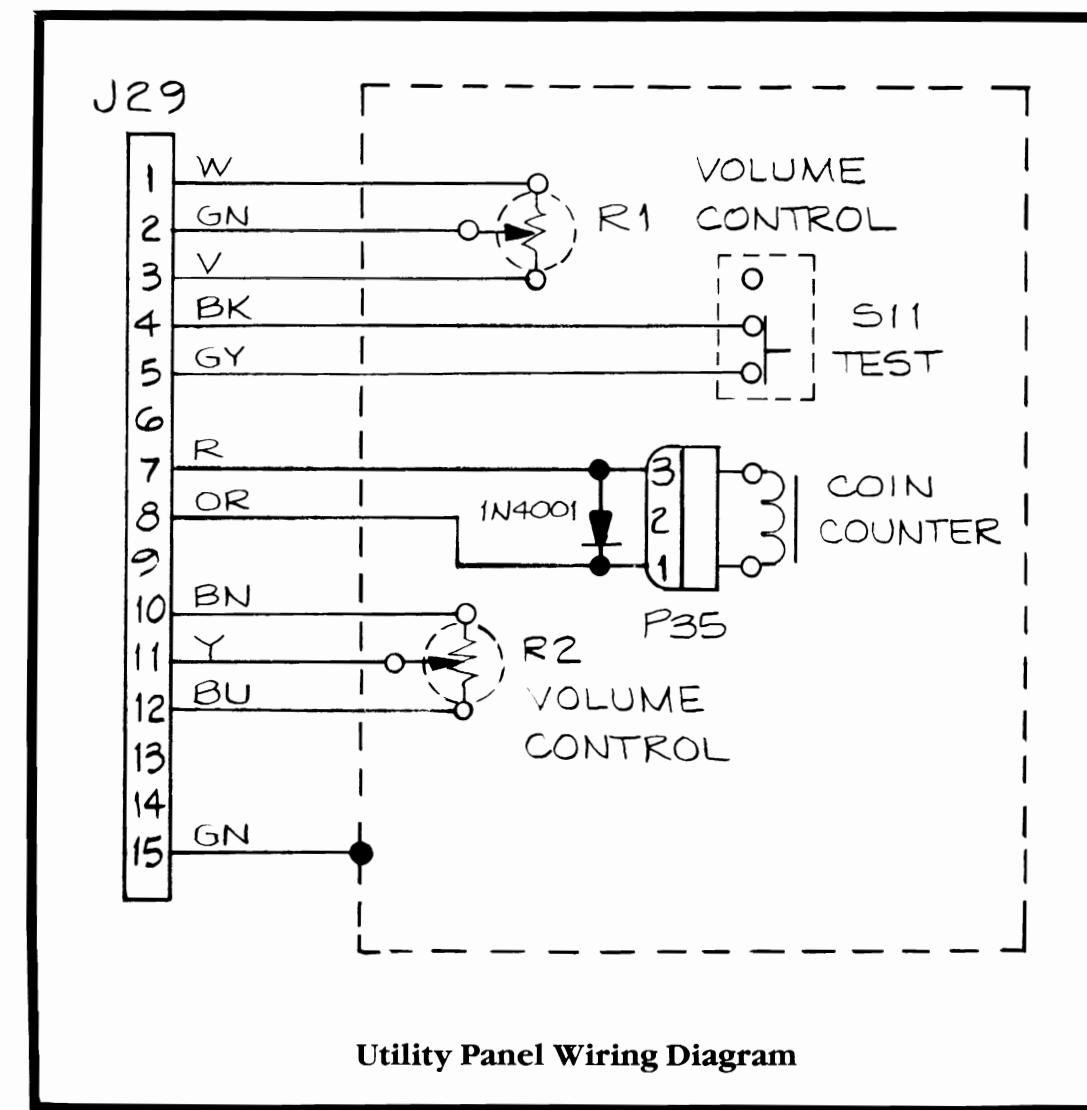
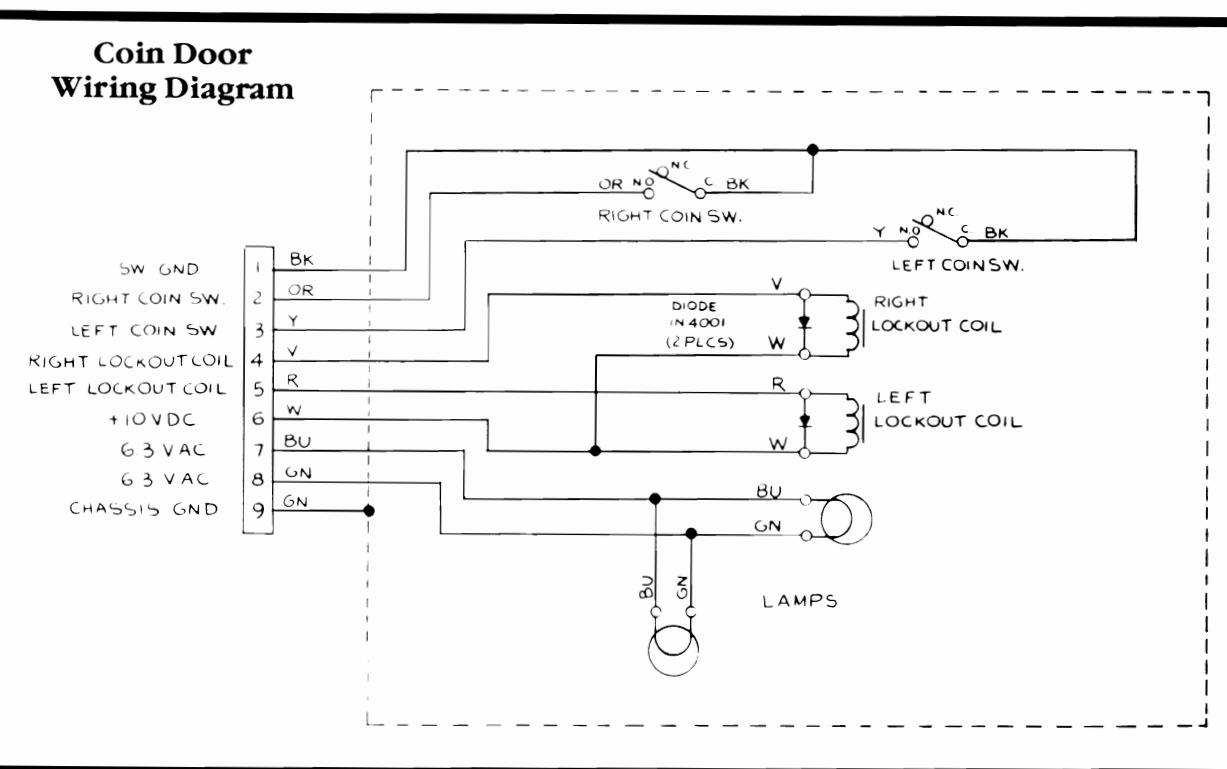
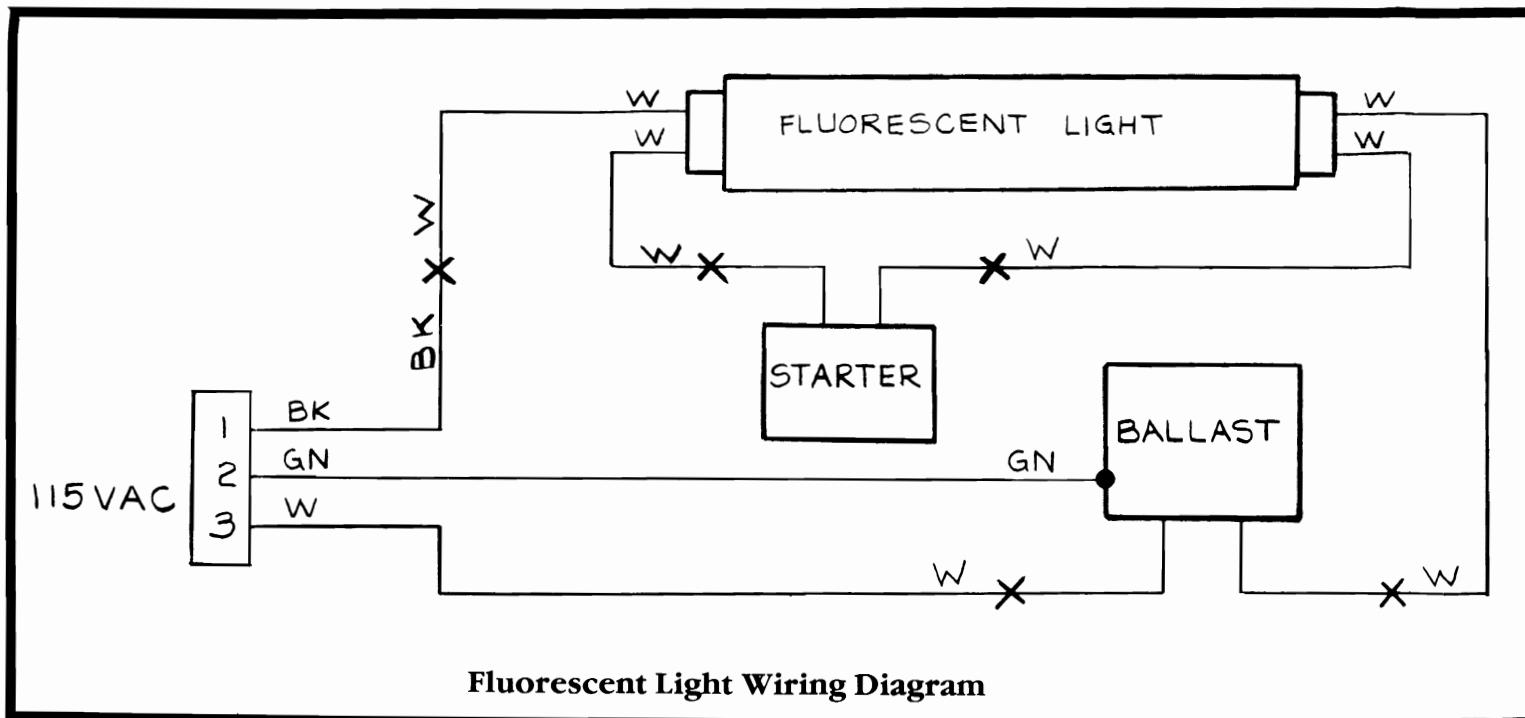


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### Paperboy™ Control Panel Wiring Diagram



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**Paperboy™  
Game Interfaces**

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## Main Microprocessor (T-11) Memory Map

Octal	Address Bus Signal Lines			Data Bus Signal Lines			Function and Size													
	A 5	A 4	A 3	A 2	A 1	A 0	R/W	5	4	3	2	1	0	R/W	5	4	3	2	1	0
000000-007777	0	0	0	0	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D
010000-010436	0	0	0	1	0	0	A	A	A	A	A	A	A	W	D	D	D	D	D	D
"	0	0	0	1	0	0	A	A	A	A	A	A	A	W	D	D	D	D	D	D
"	0	0	0	1	0	0	A	A	A	A	A	A	A	W	D	D	D	D	D	D
"	0	0	0	1	0	0	A	A	A	A	A	A	A	W	D	D	D	D	D	D
"	0	0	0	1	0	0	A	A	A	A	A	A	A	W	D	D	D	D	D	D
010000-010176	0	0	0	1	0	0	0	O	A	A	A	A	A	W	D	D	D	D	D	D
010200-010276	0	0	0	1	0	0	0	1	A	A	A	A	A	W	D	D	D	D	D	D
010400-010776	0	0	0	1	0	0	1	0	0	O	A	A	A	W	D	D	D	D	D	D
012000	0	0	0	1	0	1	0	0	0	0	0	0	0	0	W	D	D	D	D	D
012002	0	0	0	1	0	1	0	0	0	0	1	W	W	W	D	D	D	D	D	D
012200	0	0	0	1	0	1	0	0	1	W	W	W	W	W	W	W	W	W	W	W
012200-012002	0	0	0	1	0	1	0	0	A	R	W	W	W	W	D	D	D	D	D	D
012400	0	0	0	1	0	1	0	1	0	W	W	W	W	W	D	D	D	D	D	D
012600	0	0	0	1	0	1	0	1	0	0	W	W	W	W	W	W	W	W	W	W
012640	0	0	0	1	0	1	0	1	0	1	W	W	W	W	W	W	W	W	W	W
012700	0	0	0	1	0	1	0	1	1	0	W	W	W	W	W	W	W	W	W	W
012740	0	0	0	1	0	1	0	1	1	1	W	W	W	W	W	W	W	W	W	W
013000	0	0	0	1	0	1	0	0	0	W	W	W	W	W	D	D	D	D	D	D
013000	0	0	0	1	0	1	0	0	0	W	W	W	W	W	D	D	D	D	D	D
013000	0	0	0	1	0	1	0	0	0	W	W	W	W	W	D	D	D	D	D	D
013000	0	0	0	1	0	1	0	0	0	W	W	W	W	W	D	D	D	D	D	D
013200	0	0	0	1	0	1	0	1	0	W	W	W	W	W	D	D	D	D	D	D
013400	0	0	0	1	0	1	1	0	0	W	W	W	W	W	D	D	D	D	D	D
013600	0	0	0	1	0	1	1	1	1	W	W	W	W	W	D	D	D	D	D	D
014000	0	0	0	1	1	0	0	0	0	R	R	R	R	R	D	D	D	D	D	D
014000	0	0	0	1	1	0	0	0	0	R	R	R	R	R	D	D	D	D	D	D
014000	0	0	0	1	1	0	0	0	0	R	R	R	R	R	D	D	D	D	D	D
014000	0	0	0	1	1	0	0	0	0	R	R	R	R	R	D	D	D	D	D	D
014000	0	0	0	1	1	0	0	0	0	R	R	R	R	R	D	D	D	D	D	D
014000	0	0	0	1	1	0	0	0	0	R	R	R	R	R	D	D	D	D	D	D
016000	0	0	0	1	1	0	0	0	0	R	R	R	R	R	D	D	D	D	D	D
020000-033776	0	0	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D
"	0	0	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D
034000-037776	0	0	1	1	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D
"	0	0	1	1	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D
"	0	0	1	1	A	A	A	A	A	0	0	0	0	R/W	D	D	D	D	D	D
"	0	0	1	1	A	A	A	A	A	1	0	0	0	R/W	D	D	D	D	D	D
"	0	0	1	1	A	A	A	A	A	1	1	0	0	R/W	D	D	D	D	D	D
020000-037776	0	0	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D
020000-037776	0	0	1	A	A	A	A	A	A	A	A	A	A	R/W	D	D	D	D	D	D
040000-057776	0	1	0	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D
060000-077776	0	1	1	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D
100000-177776	1	A	A	A	A	A	A	A	A	A	A	A	A	R	D	D	D	D	D	D

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Paperboy™ Main Microprocessor Memory Map



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SP-275 Sheet 4A  
1st printing

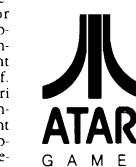
## Sound Microprocessor (6502) Memory Map

Hexa-decimal	A A A A A A A A A A A A A A A A		D D D D D D D D		Function and Size
	1 1 1 1 1 1 0 0 0 0 0 0 0 0 0 0		0 0 0 0 0 0 0		
	5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0	R/W	7 6 5 4 3 2 1 0		
0000-0FFF	0 0 0 0 A A A A A A A A A A A A	R/W	D D D D D D D D		Program RAM (4KB)
1000-17FF	0 0 0 1 0 A A A A A A A A A A A A	R/W	D D D D D D D D		EEROM (2KB)
1800-180F	0 0 1 1 0 0 0 A A A A A A A A A A	R/W	D D D D D D D D		POKEY 1 (16B)
1810-1813	0 0 1 1 0 0 1 A A A A A A A A A A	R	D D D D D D D D		LETA
1830-183F	0 0 1 1 0 1 1 A A A A A A A A A A	R/W	D D D D D D D D		POKEY 2 (16B)
1840	0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0	R		D	T-11 Talk (Active High)
1840	0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0	R		D	6502 Talk (Active High)
1840	0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0	R		D	TI READY (Active High)
1840	0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0	R		D	
1840	0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0	R		D	Self-Test Switch (Active Low)
1840	0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0	R		D	Auxiliary Coin Switch (Active Low)
1840	0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0	R		D	Left Coin Switch (Active Low)
1840	0 0 1 1 1 0 0 0 0 0 0 0 0 0 0 0	R		D	Right Coin Switch (Active Low)
1850-1851	0 0 1 1 1 0 1 0 0 0 0 0 0 0 0 0	A	R	D D D D D D D D	Yamaha Sound Chip
1860	0 0 1 1 1 1 0 0 0 0 0 0 0 0 0 0	R	D D D D D D D D		Communications Port Read
1870	0 0 1 1 1 1 1 0 0 0 0 0 0 0 0 0	W	D D D D D D D D		TI Data
1872-1873	0 0 1 1 1 1 1 0 0 1 0 0 0 0 0 0	A	W	D D D D D D D D	TI Write Enable
1874	0 0 1 1 1 1 1 0 1 0 0 0 0 0 0 0	W	D D D D D D D D		Communications Port Write
1876	0 0 1 1 1 1 1 0 1 1 0 0 0 0 0 0	W		D	Right Coin Counter (Active High)
1876	0 0 1 1 1 1 1 0 1 1 0 0 0 0 0 0	W		D	Left Coin Counter (Active High)
1878	0 0 1 1 1 1 1 1 0 0 0 0 0 0 0 0	W			IRQ Clear
187A	0 0 1 1 1 1 1 1 0 1 0 0 0 0 0 0	W		D D D	Yamaha Mixer
187A	0 0 1 1 1 1 1 1 0 1 0 0 0 0 0 0	W		D D	POKEY Mixer
187A	0 0 1 1 1 1 1 1 0 1 0 0 0 0 0 0	W	D D D		TI Sound Mixer
187C	0 0 1 1 1 1 1 1 1 0 0 0 0 0 0 0	W		D	LED 1
187C	0 0 1 1 1 1 1 1 1 0 0 0 0 0 0 0	W		D	LED 2
187C	0 0 1 1 1 1 1 1 1 0 0 0 0 0 0 0	W		D	LETA Resolution
187C	0 0 1 1 1 1 1 1 1 0 0 0 0 0 0 0	W		D	TI Frequency Select
187E	0 0 1 1 1 1 1 1 1 1 0 0 0 0 0 0	W		D	Sound Enable (Active High)
4000-7FFF	0 1 A A A A A A A A A A A A A A A A	R	D D D D D D D D		Program ROM (16KB)
8000-FFFF	1 A A A A A A A A A A A A A A A A	R	D D D D D D D D		Program ROM (32KB)

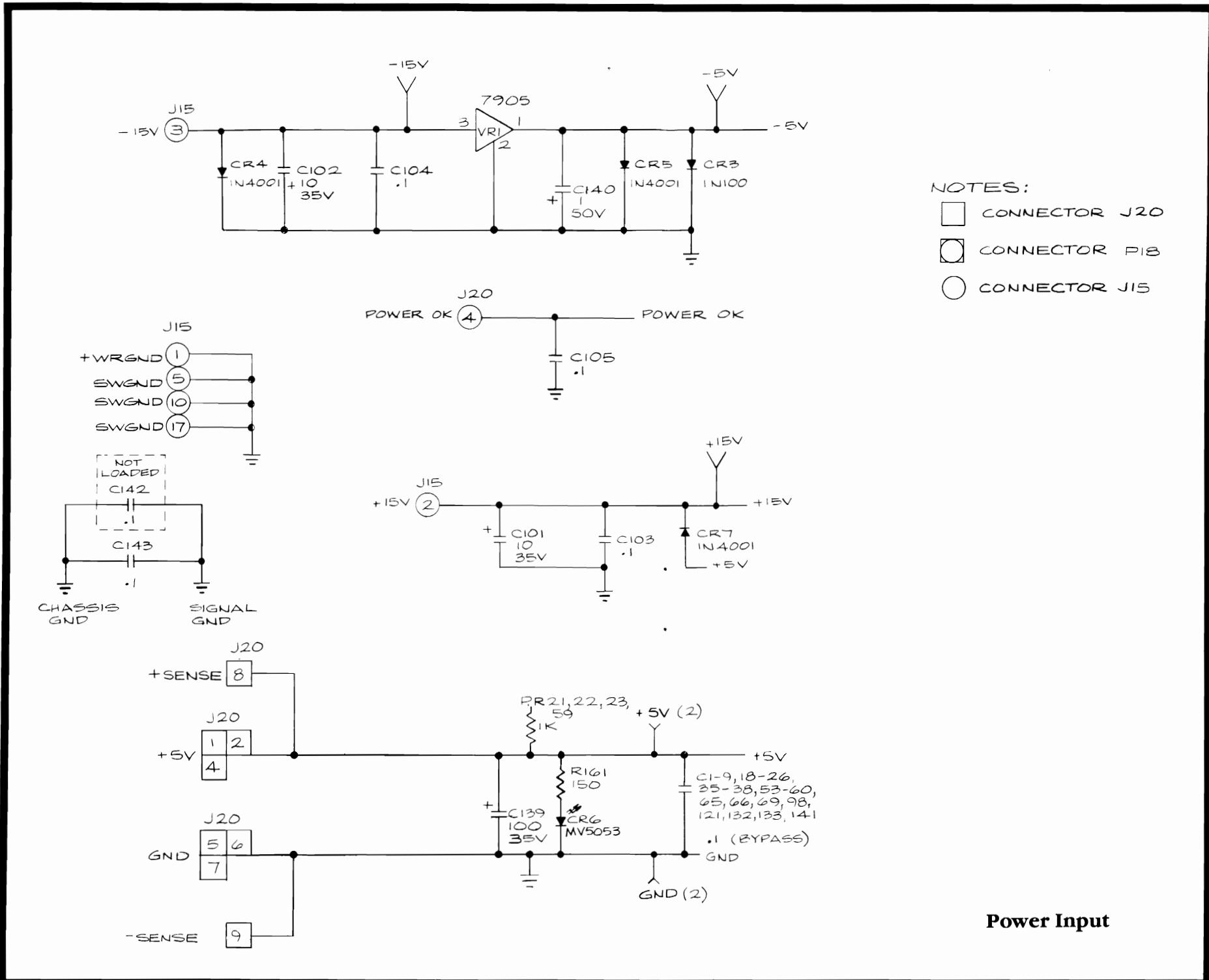
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Paperboy™ Sound  
Microprocessor Memory Map



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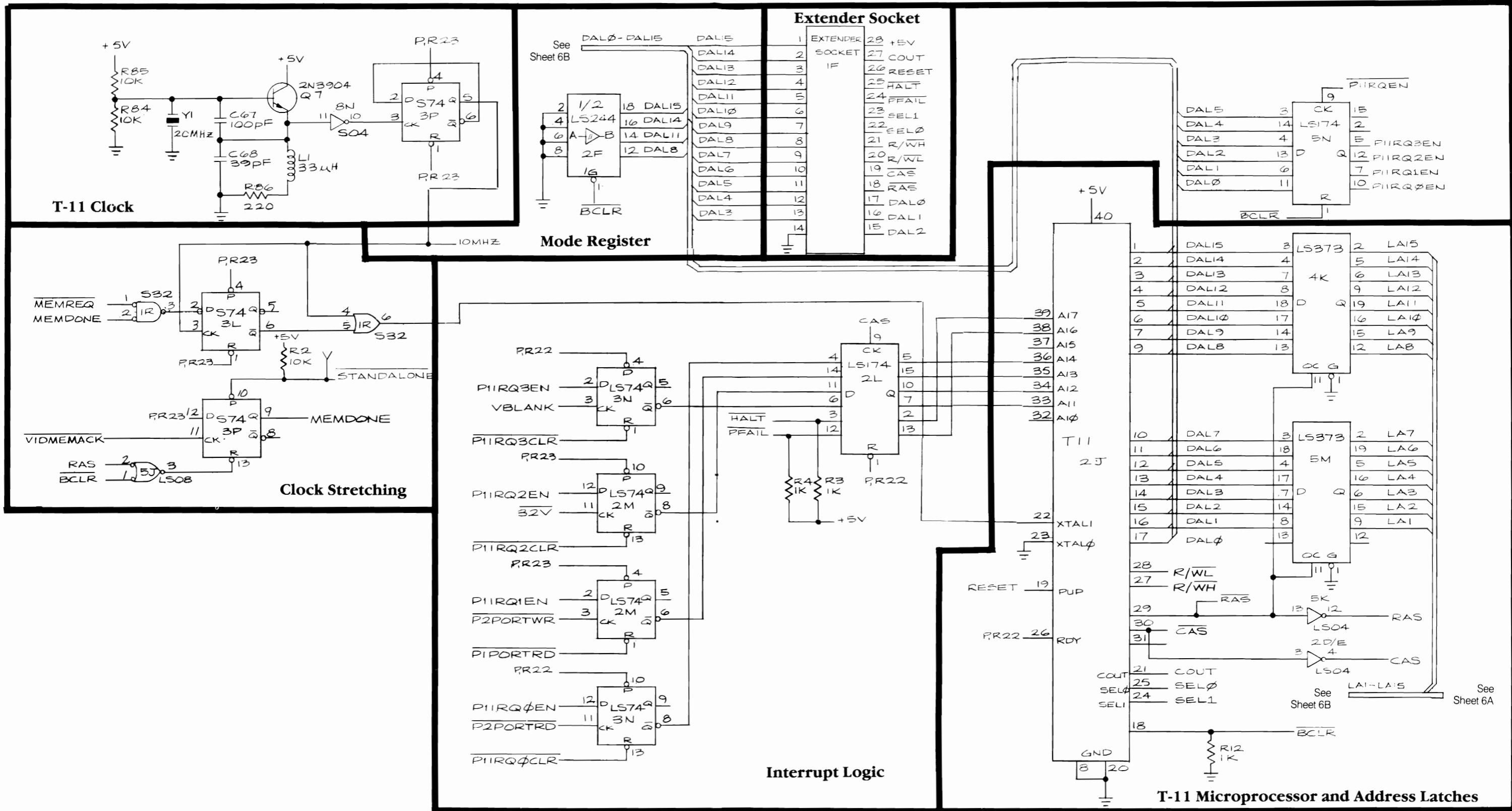


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### Paperboy™ CPU PCB Schematic Diagram



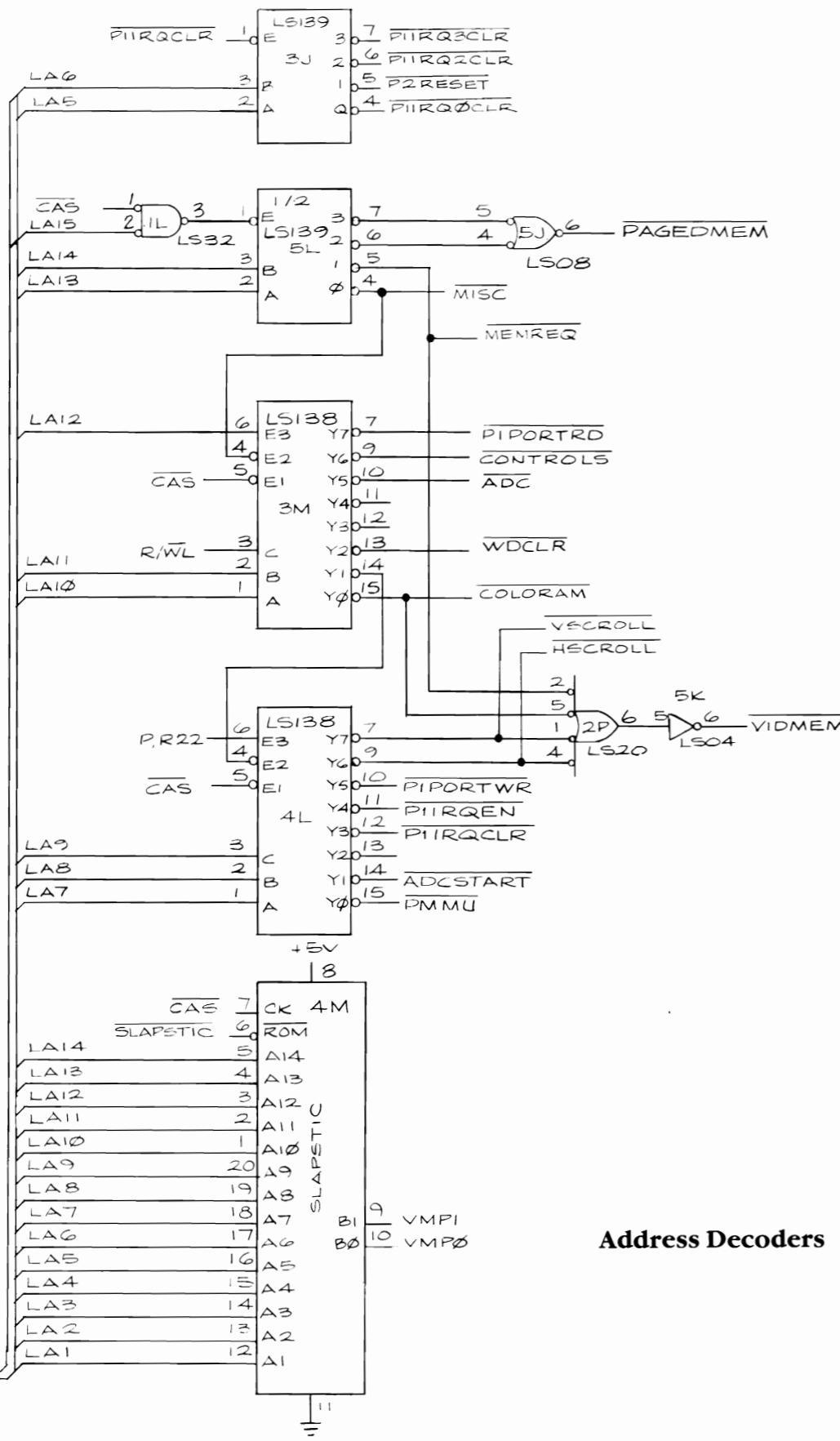
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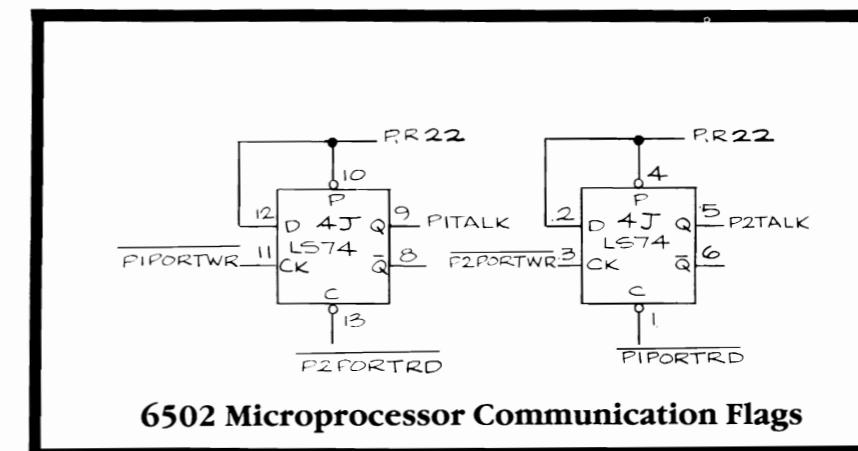
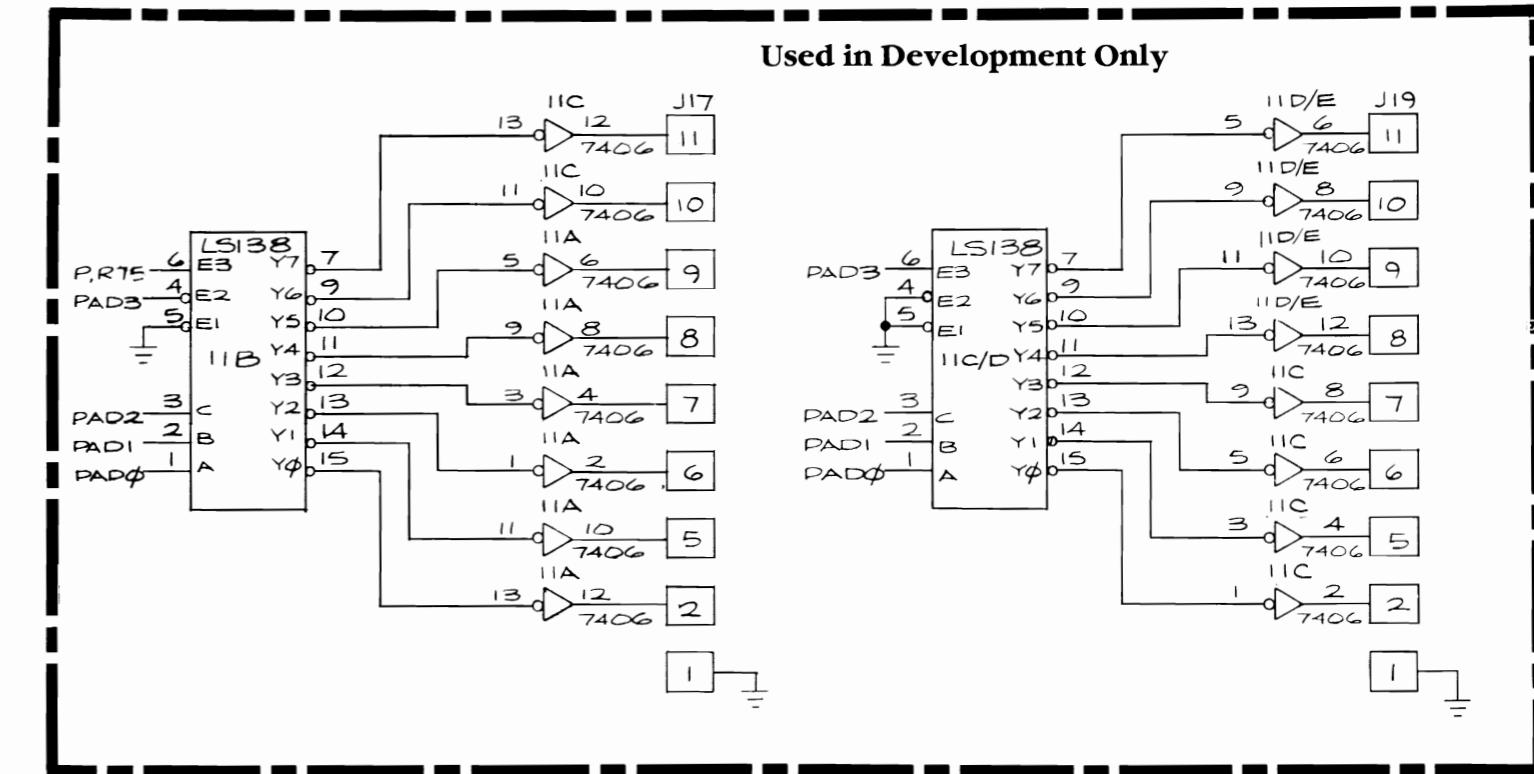


**Paperboy™ CPU PCB Schematic Diagram**



## Address Decoder

See  
Sheets  
6B & 7A



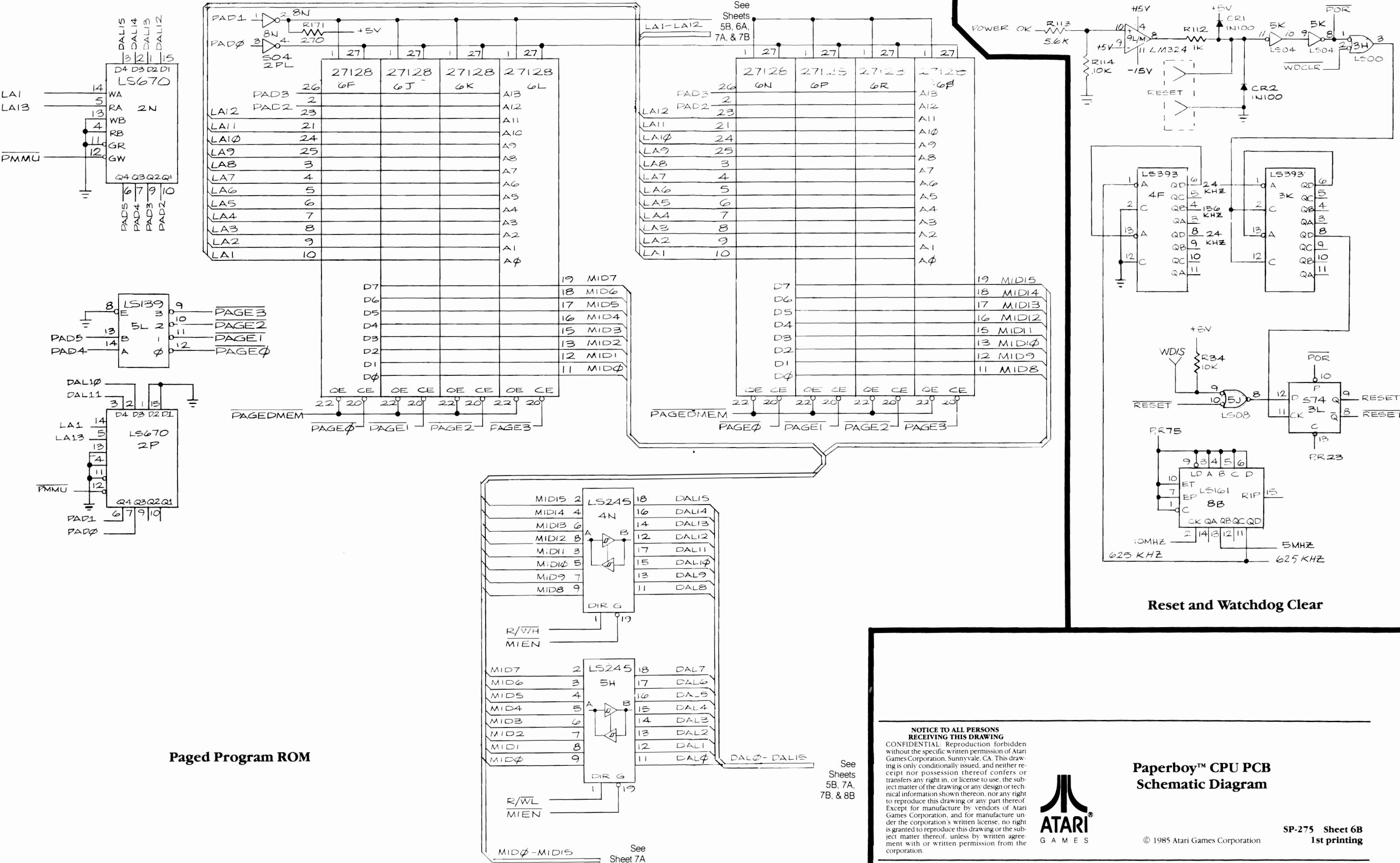
## 6502 Microprocessor Communication Flags

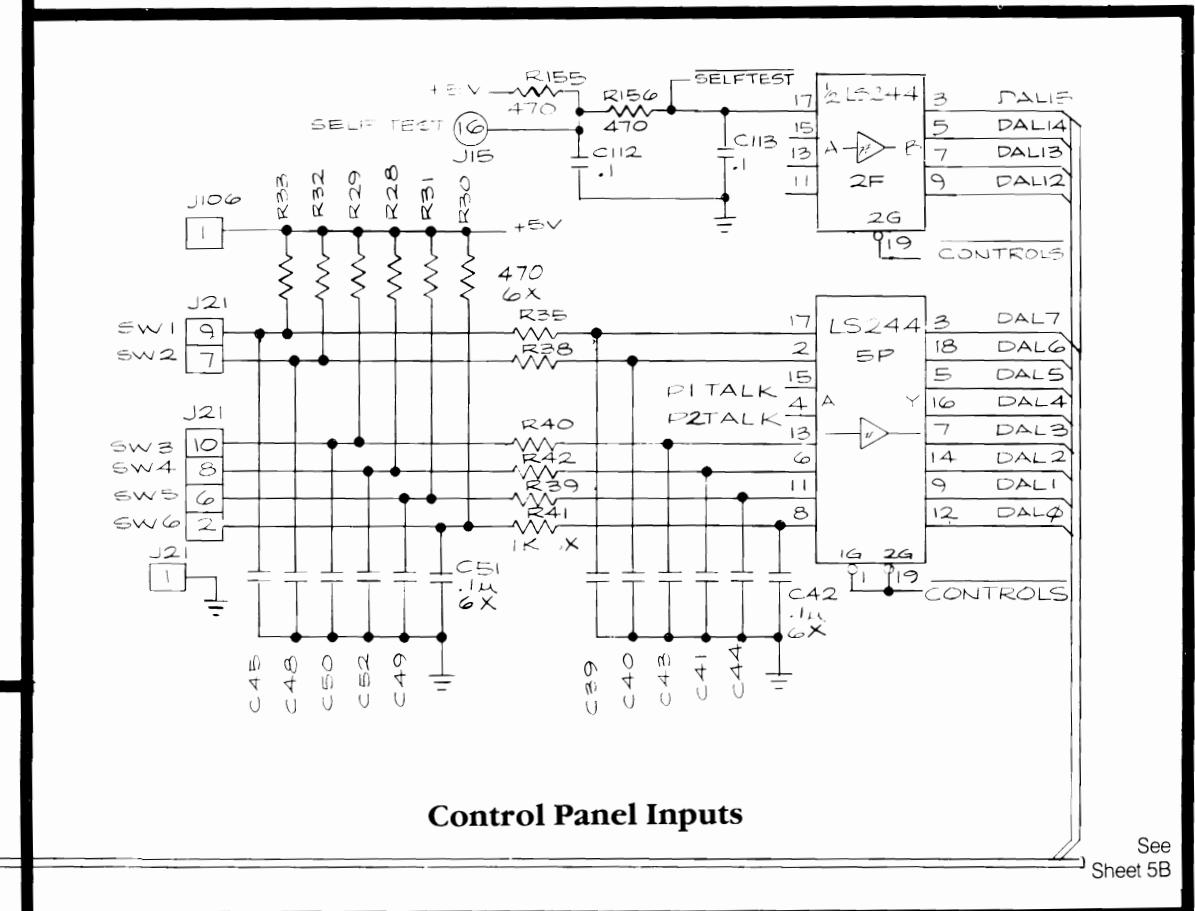
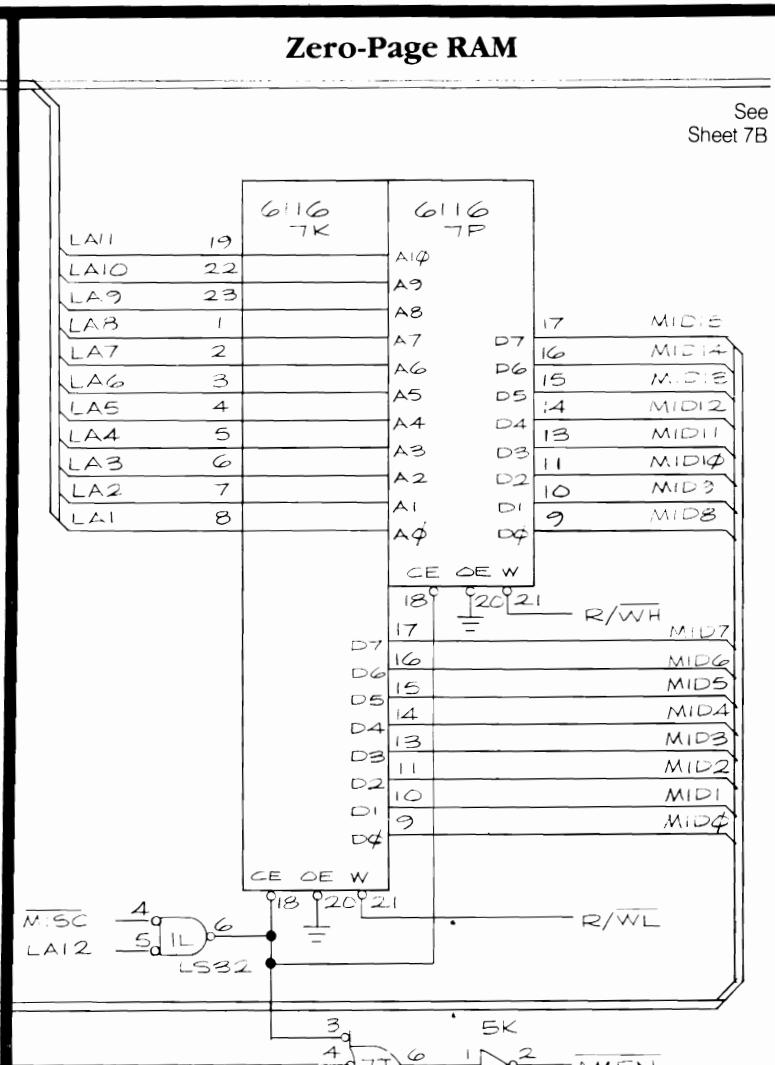
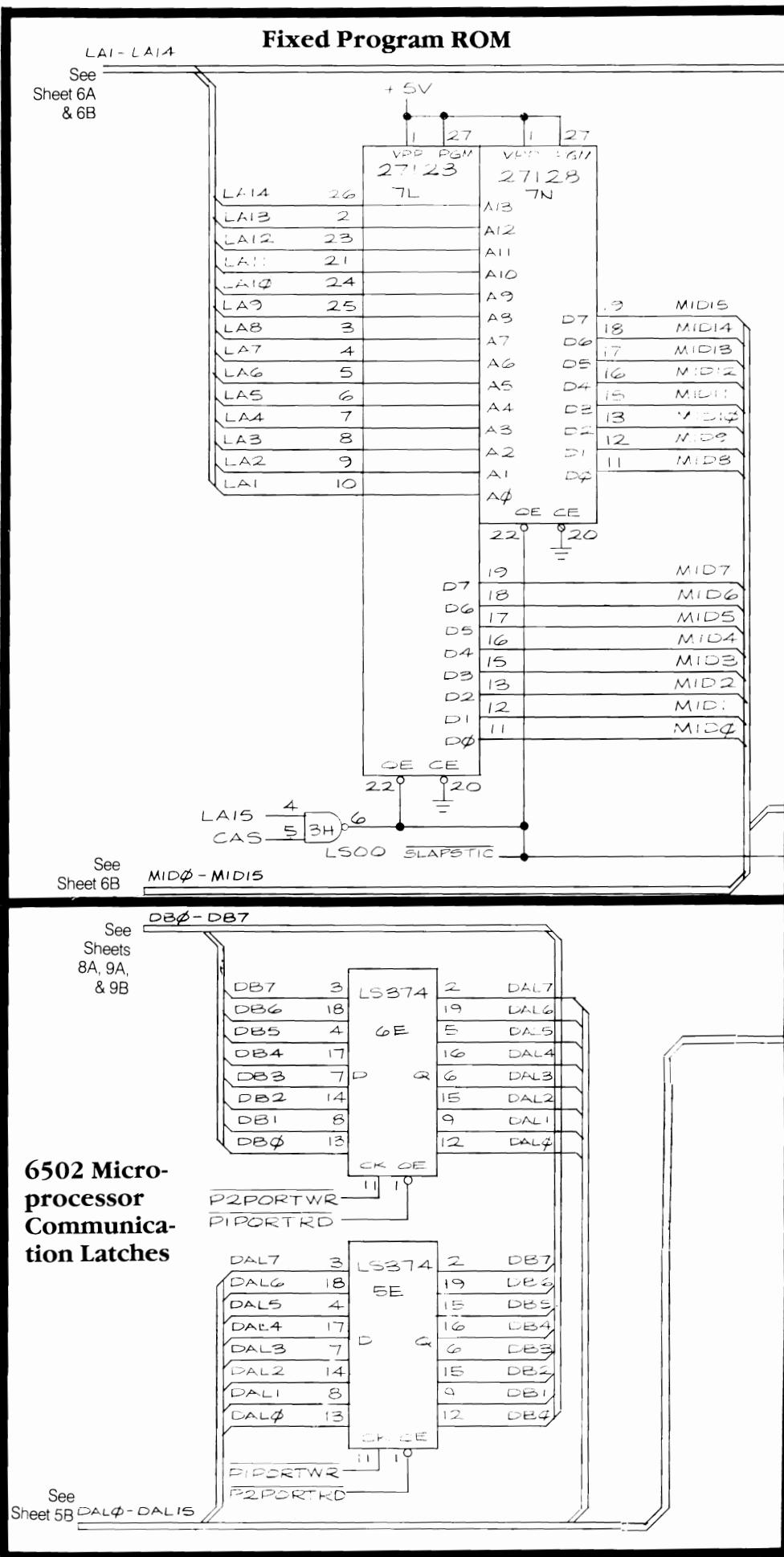
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# Paperboy™ CPU PCB Schematic Diagram







## **6502 Micro-processor Communication Latches**

See Sheet 5B DALΦ - DALΙ

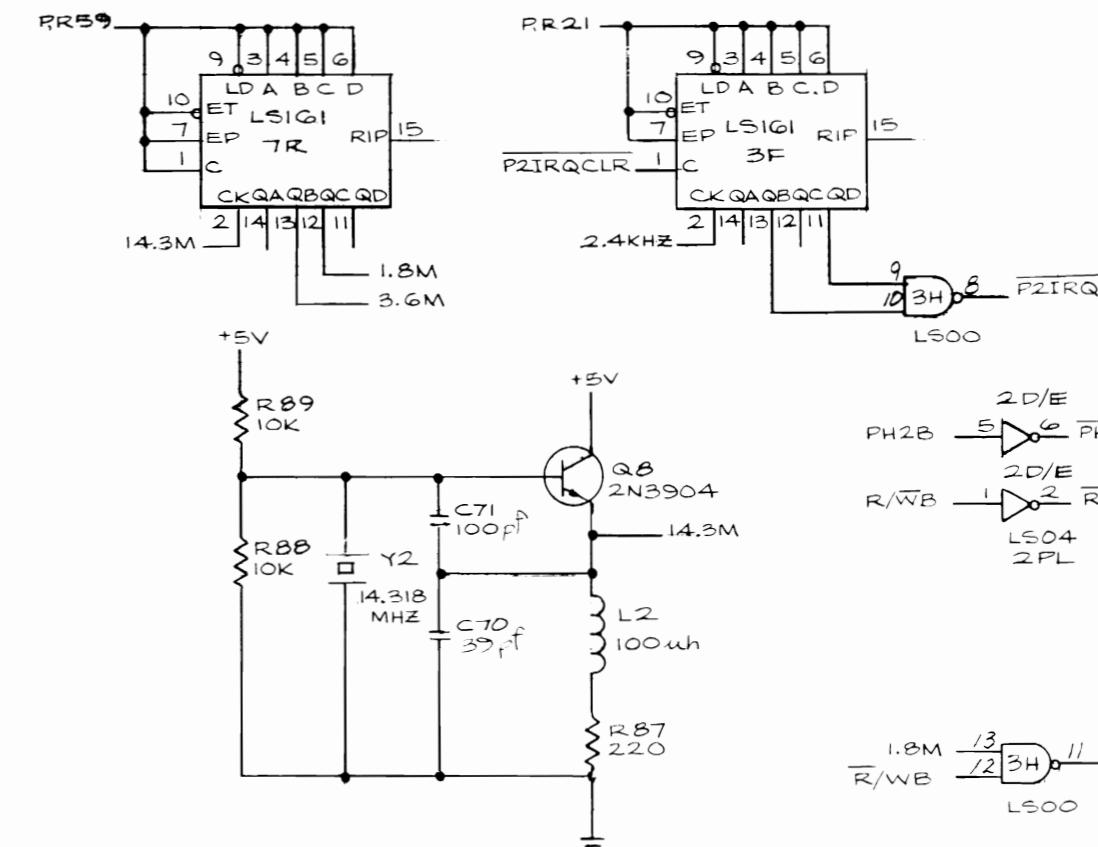
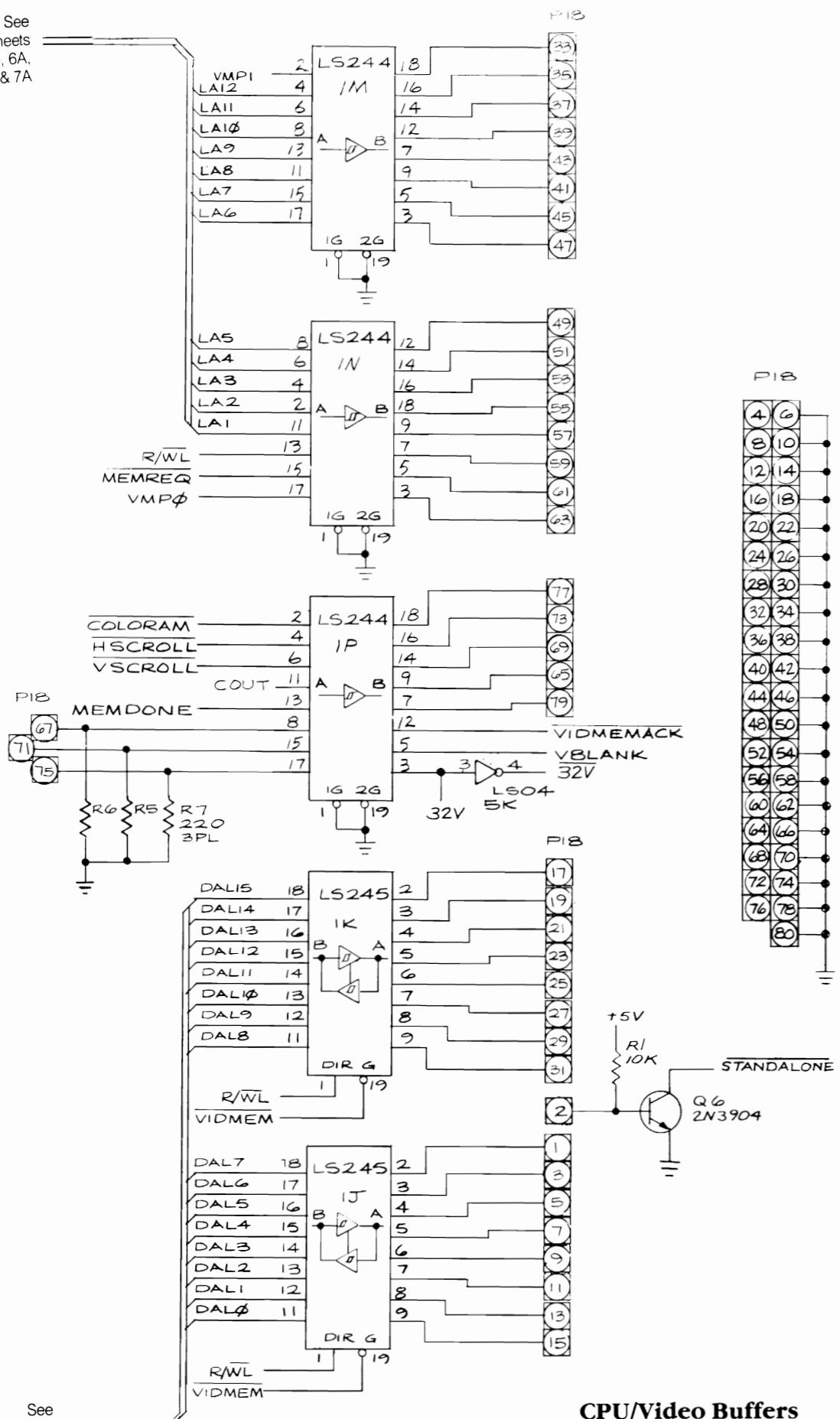
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# Paperboy™ CPU PCB Schematic Diagram



See  
Sheets  
5B, 6A,  
& 7A

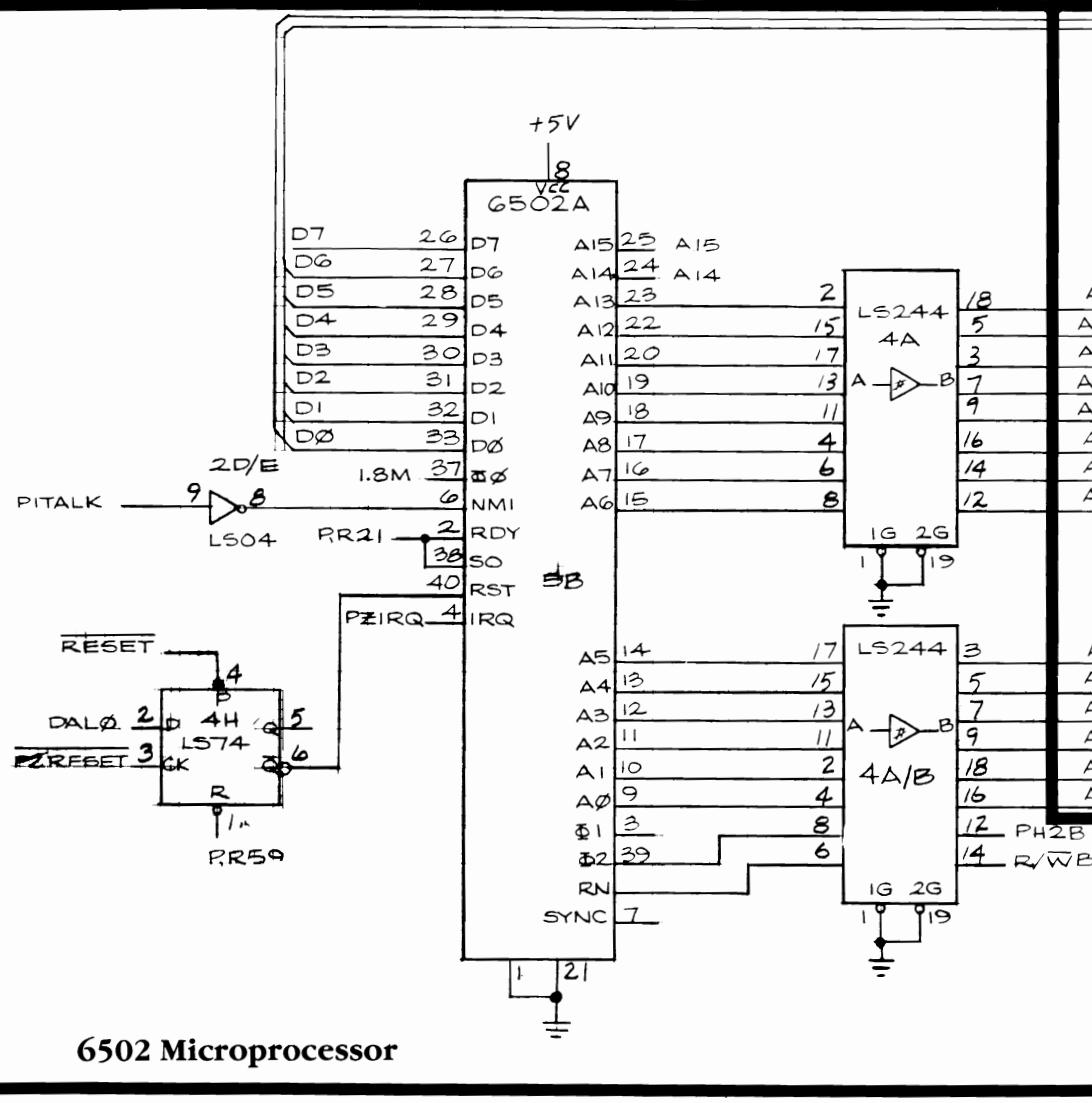


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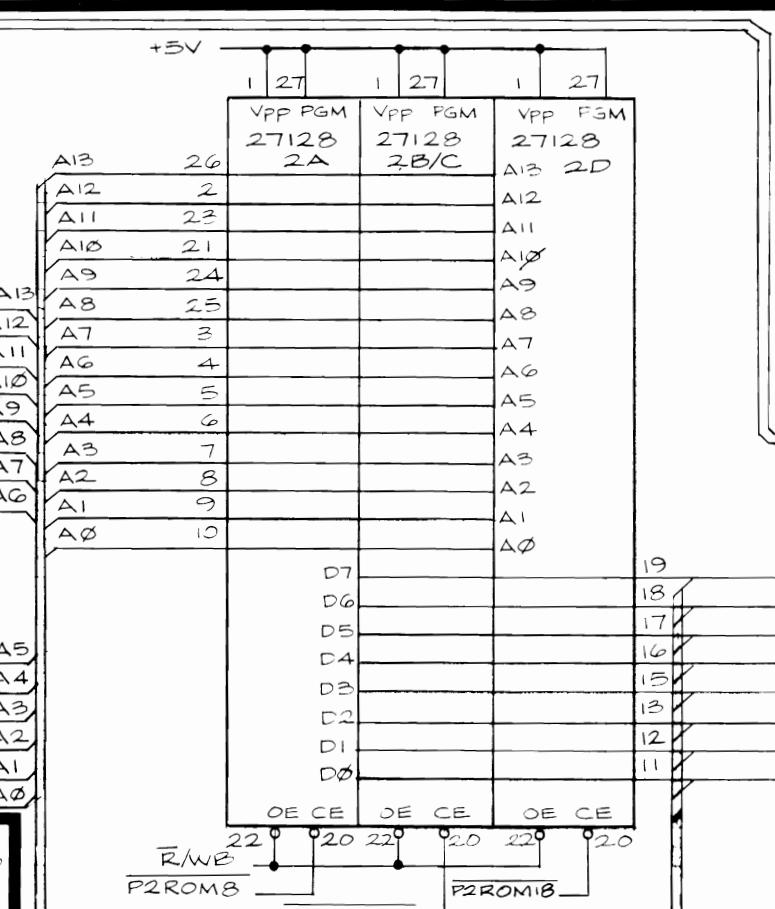
**Paperboy™ CPU PCB Schematic Diagram**



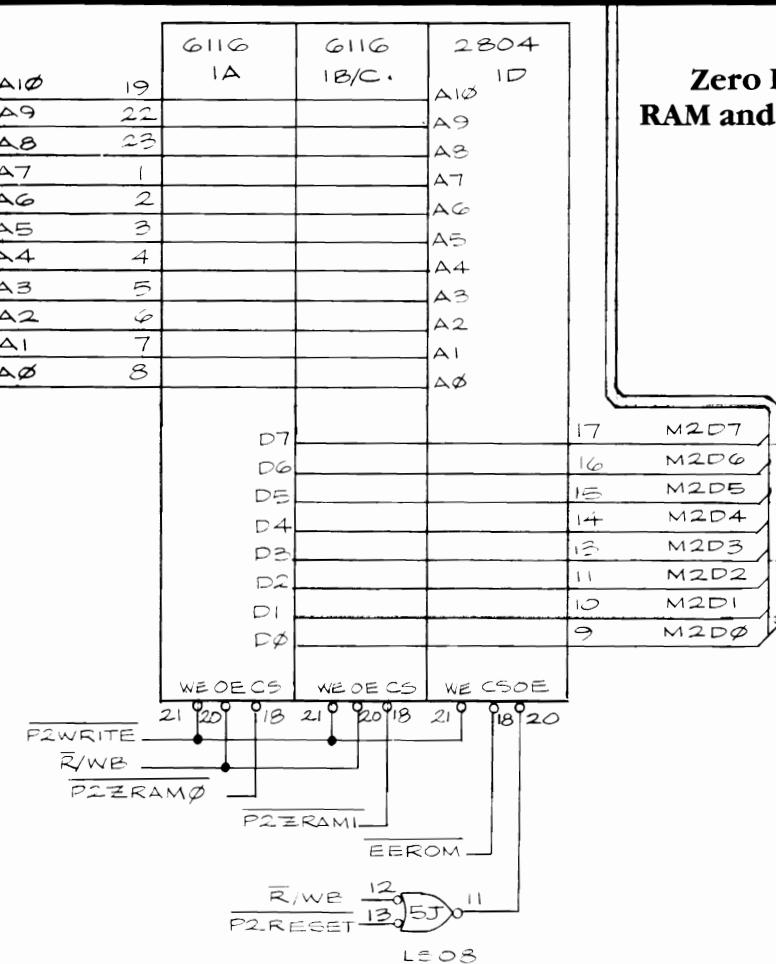
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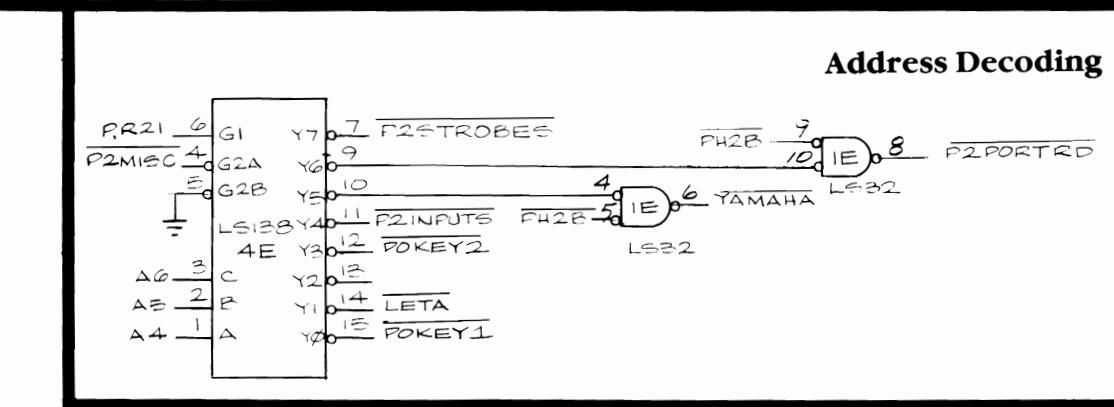
## **6502 Microprocessor**



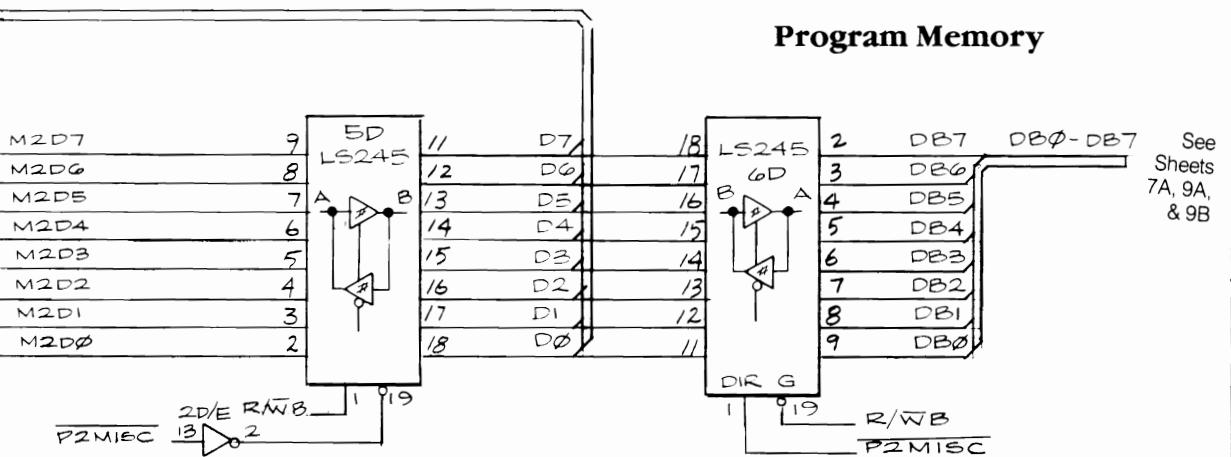
## **Zero Page RAM and EEPROM**



## Address Decoding



## Program Memory



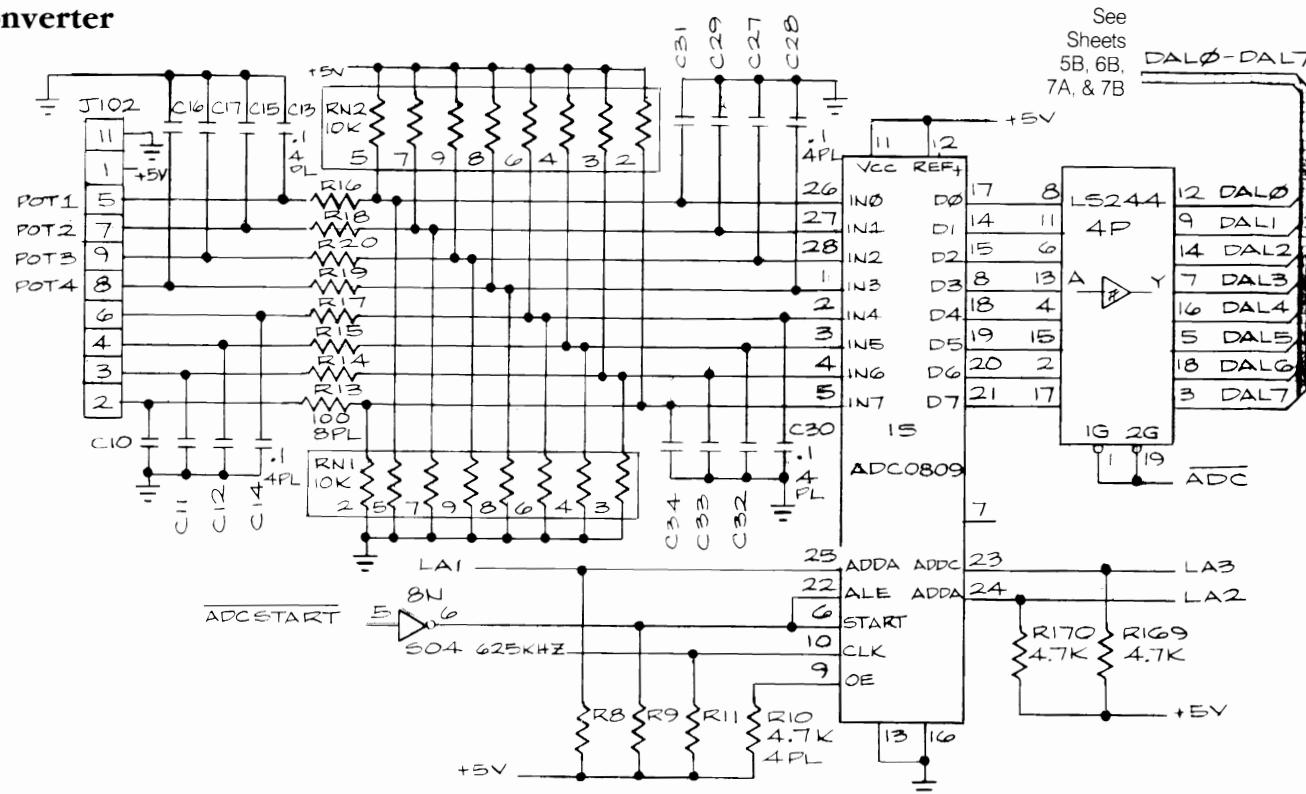
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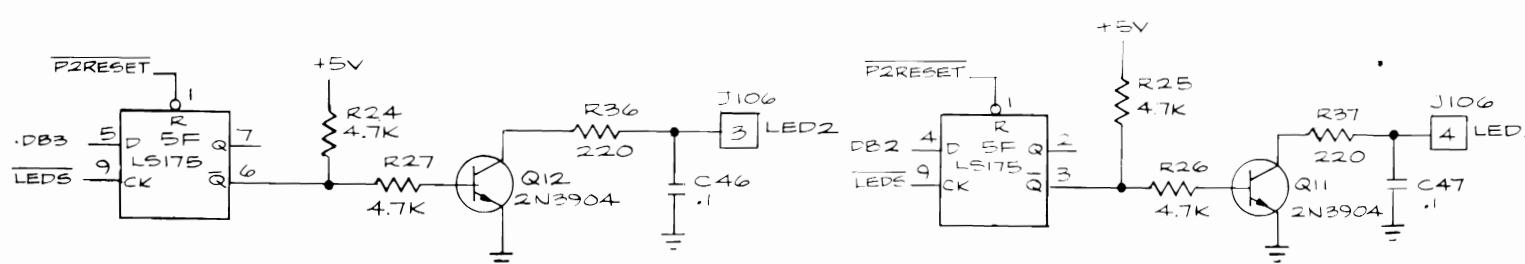
# Paperboy™ CPU PCB Schematic Diagram



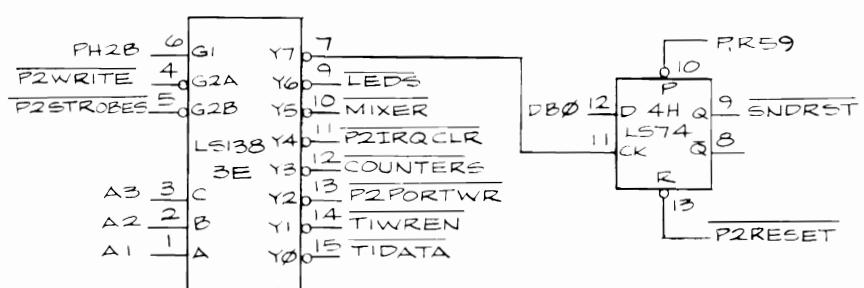
## A/D Converter



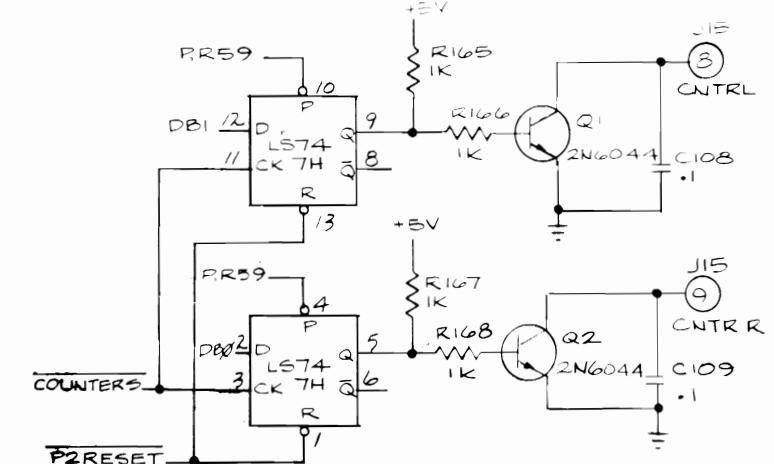
LED Drivers



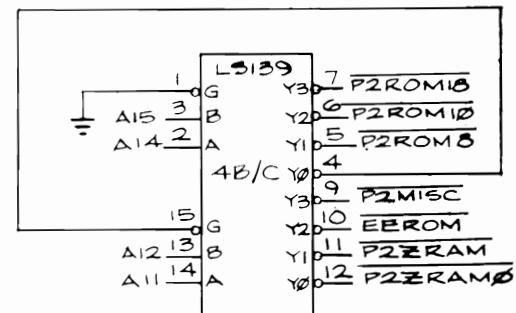
## Address Decoding



## Coin Counters



## Address Decoding

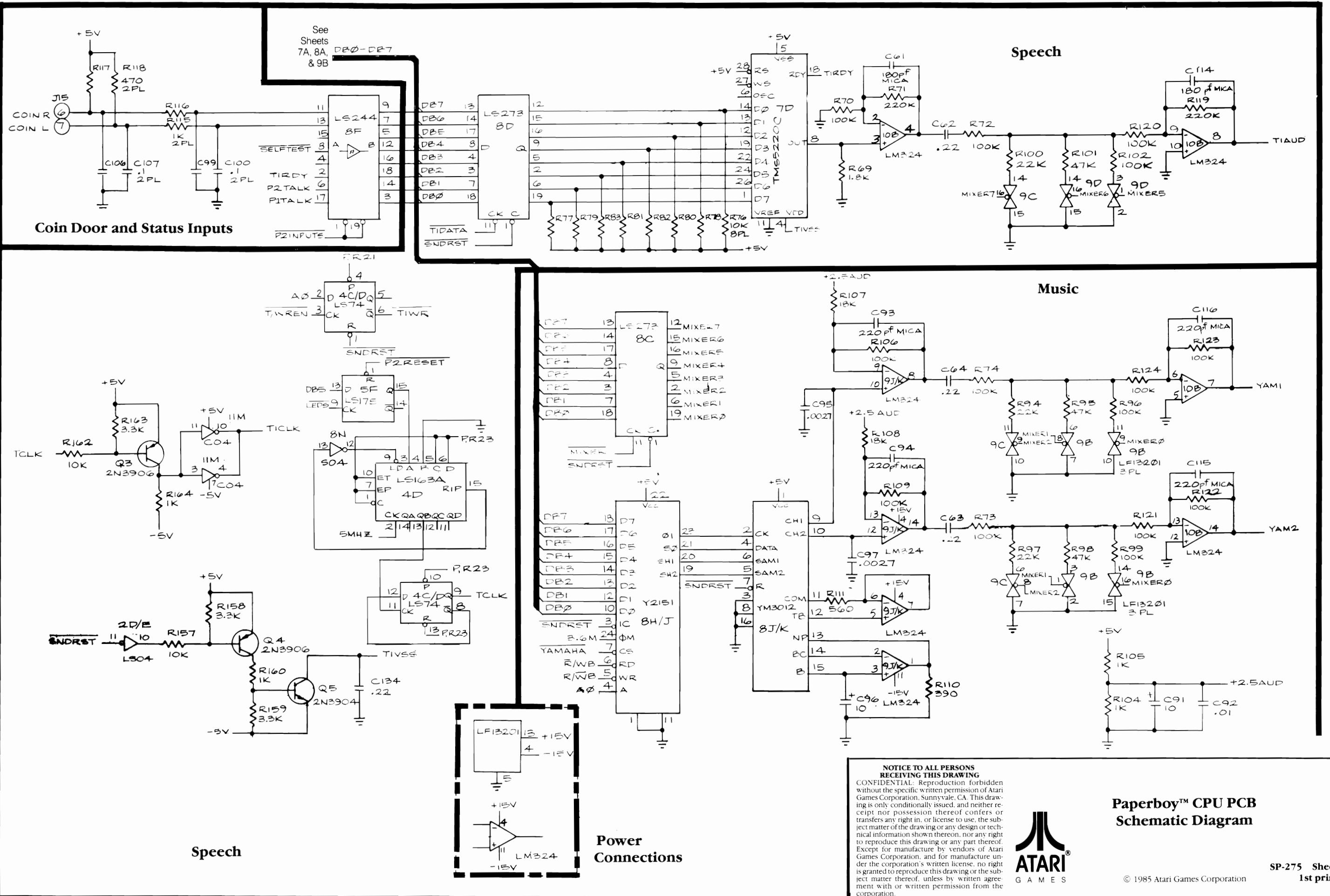


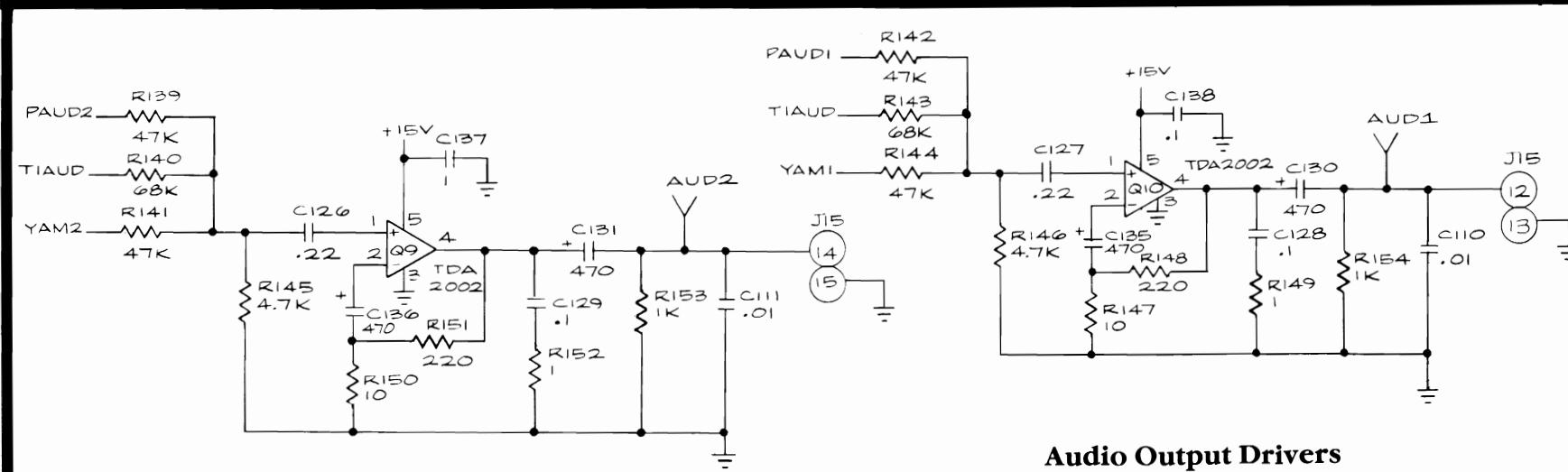
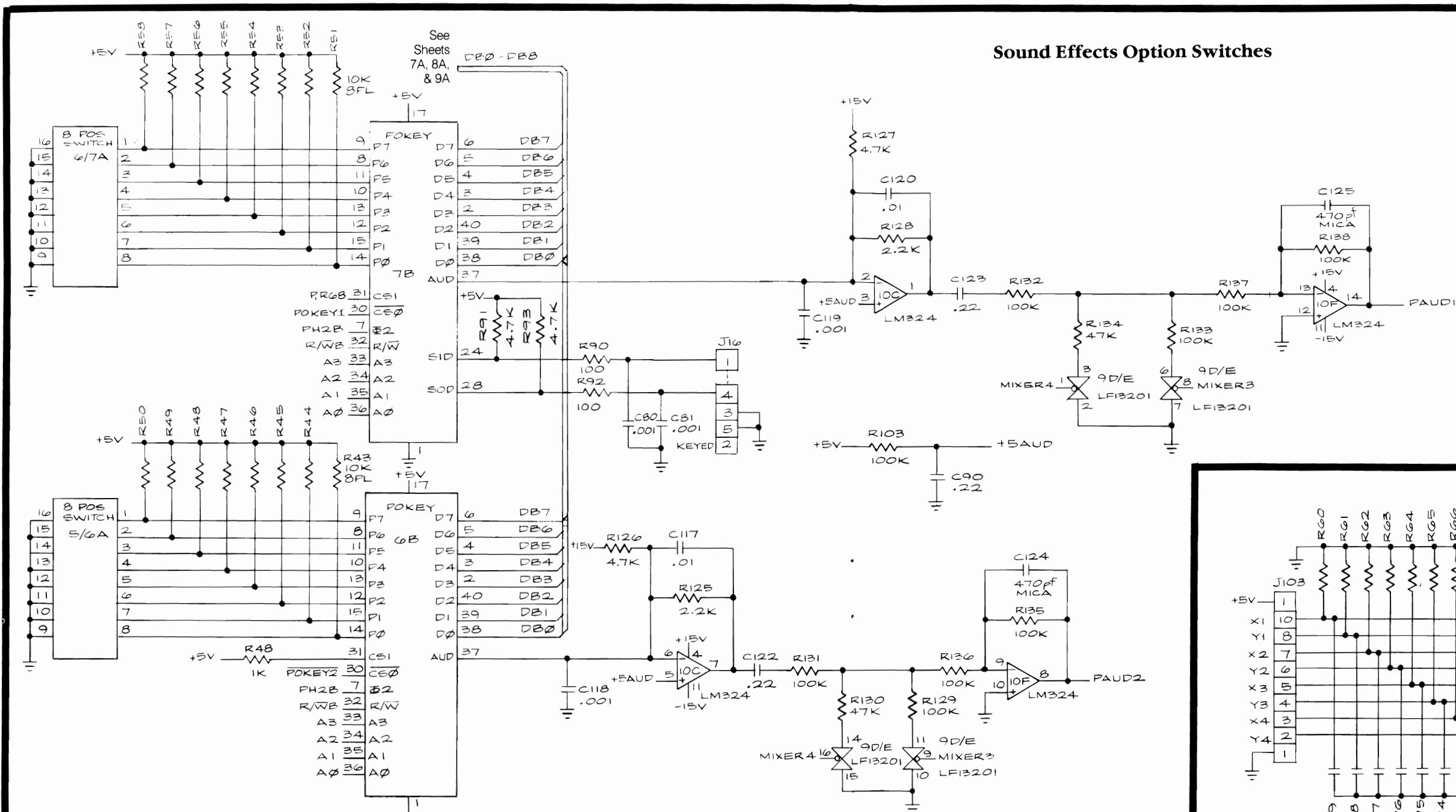
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# Paperboy™ CPU PCB Schematic Diagram



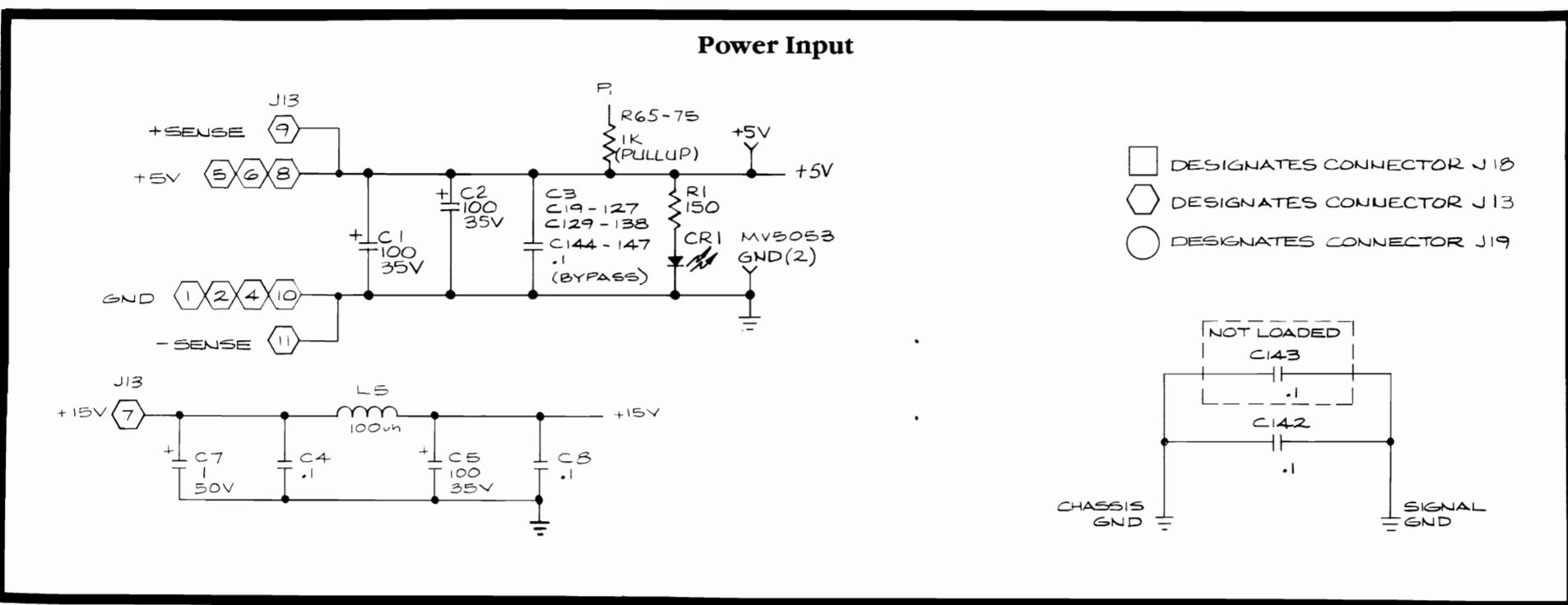




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**Paperboy™ CPU PCB Schematic Diagram**

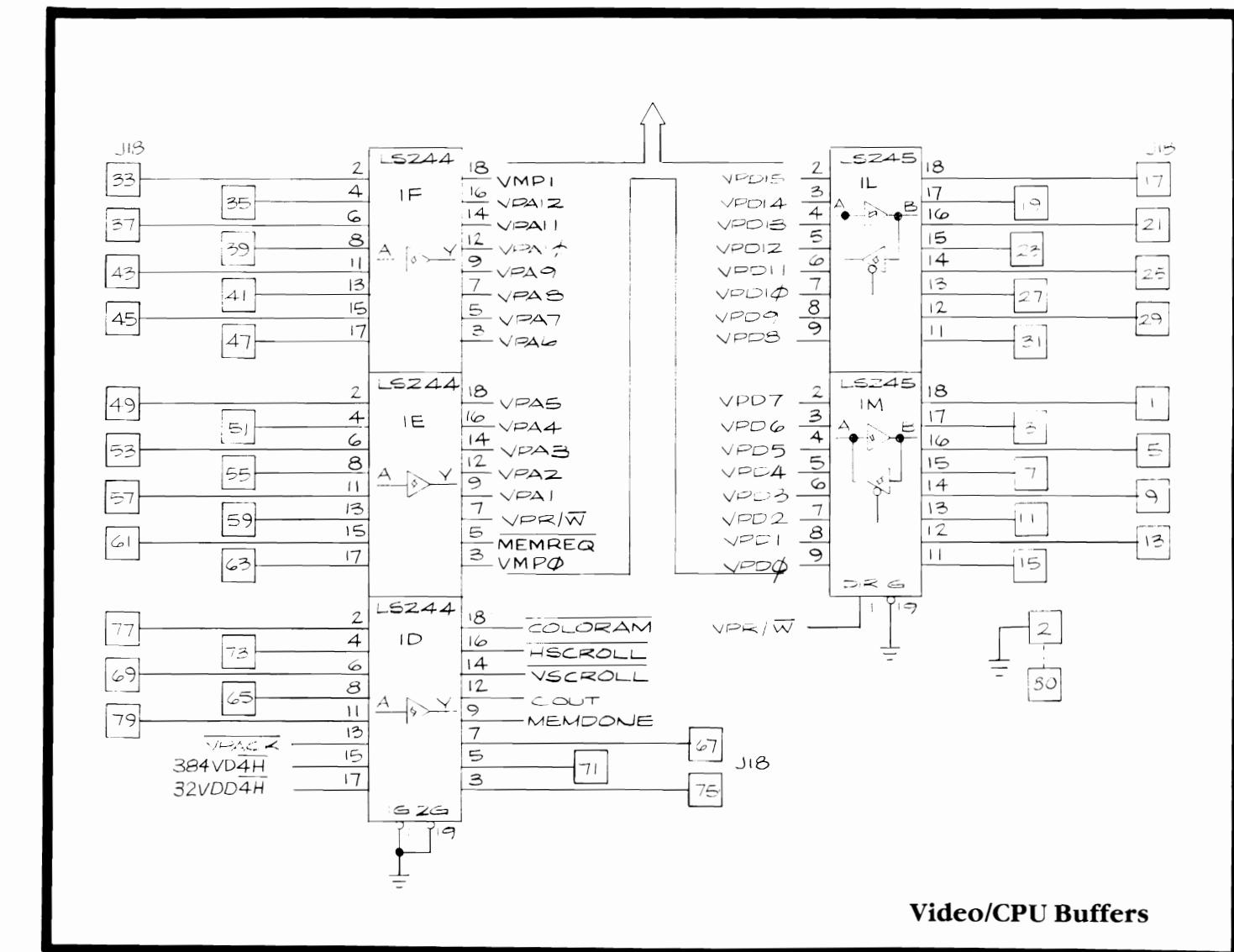
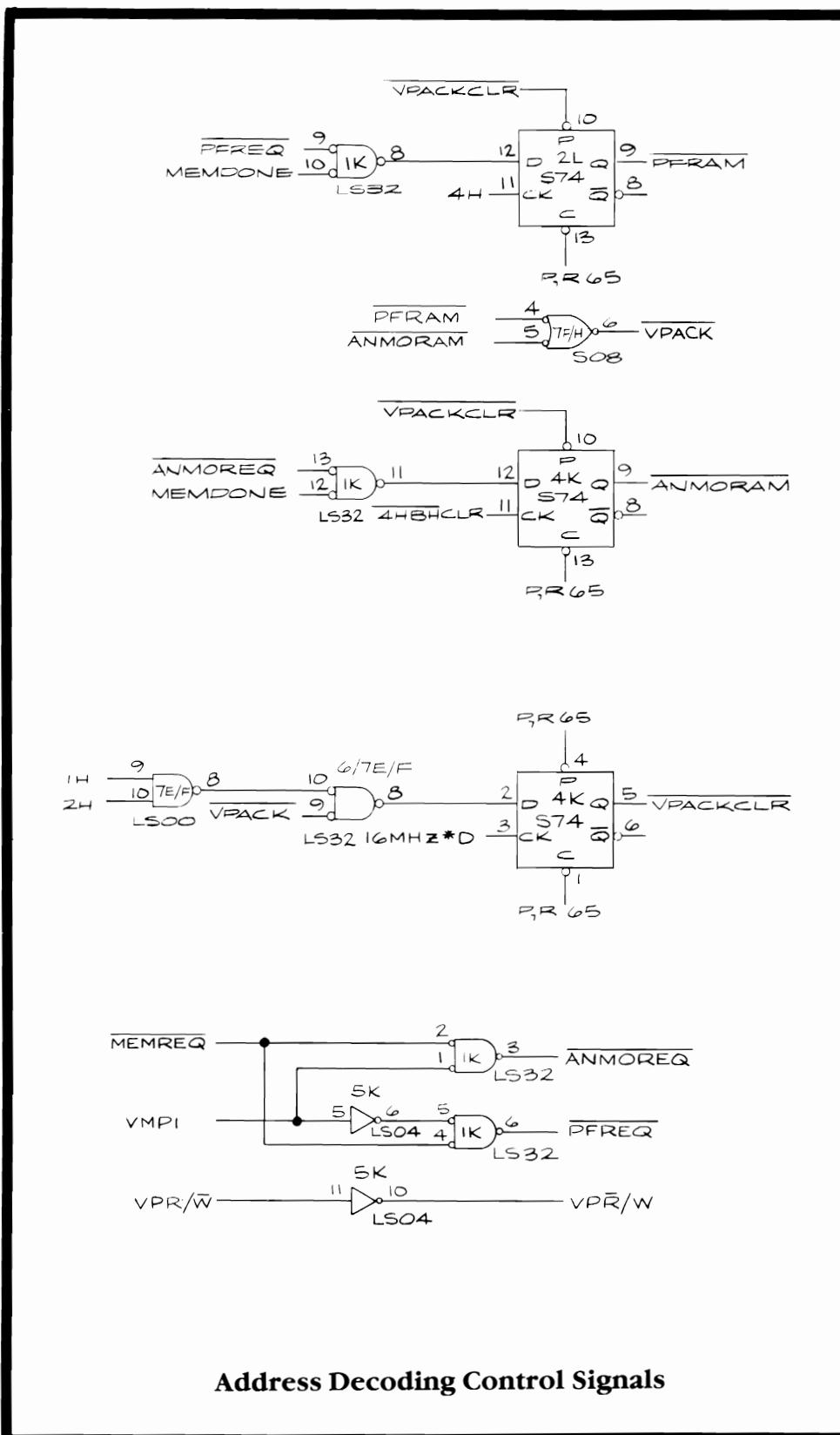


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**Paperboy™ Video PCB  
Schematic Diagram**

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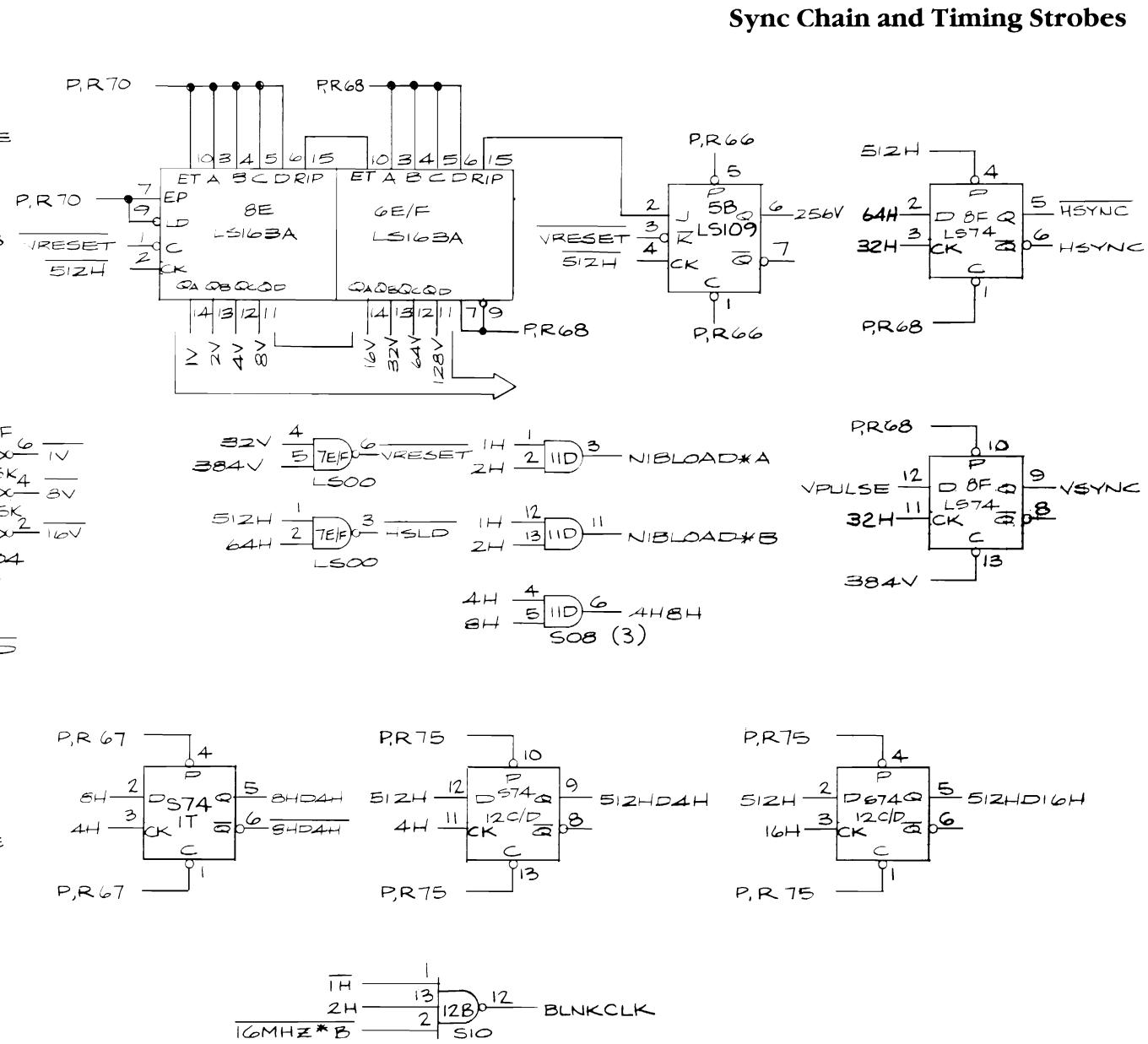
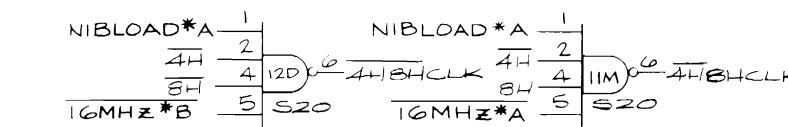
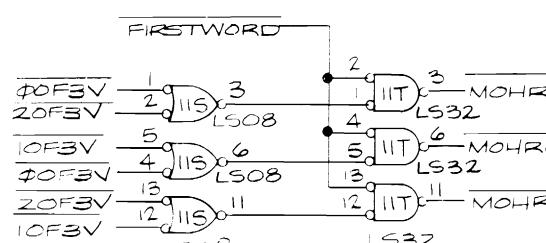
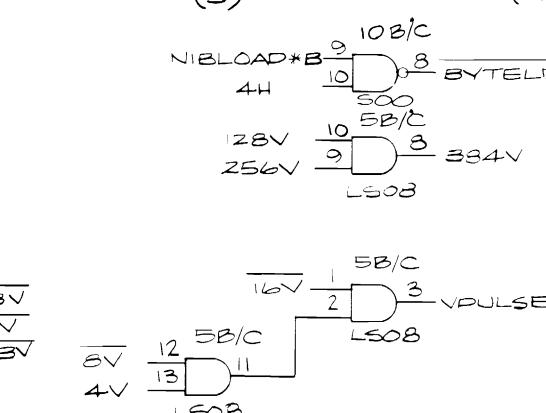
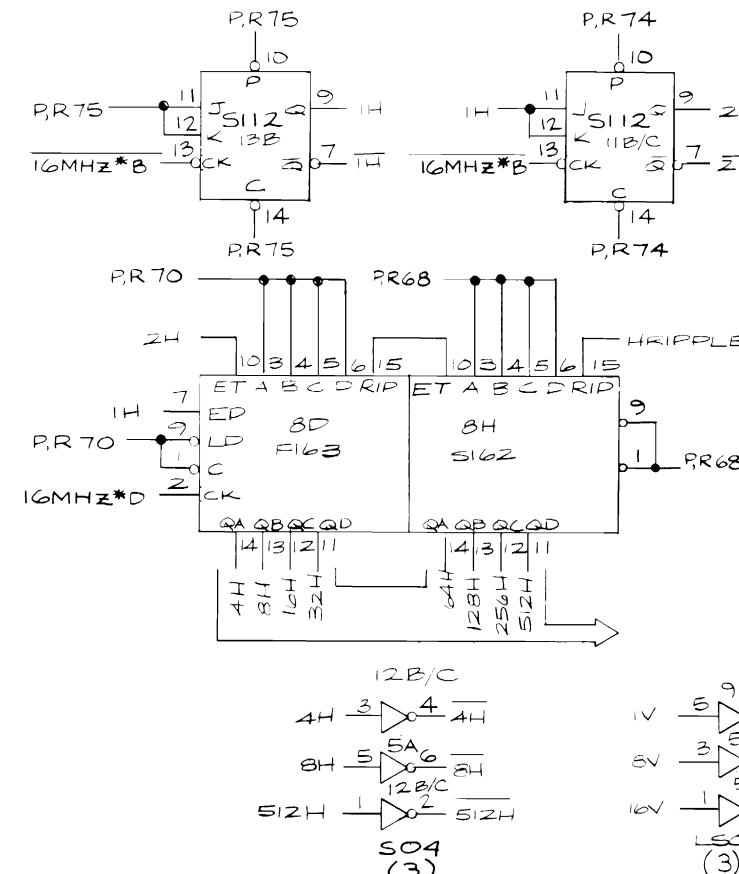
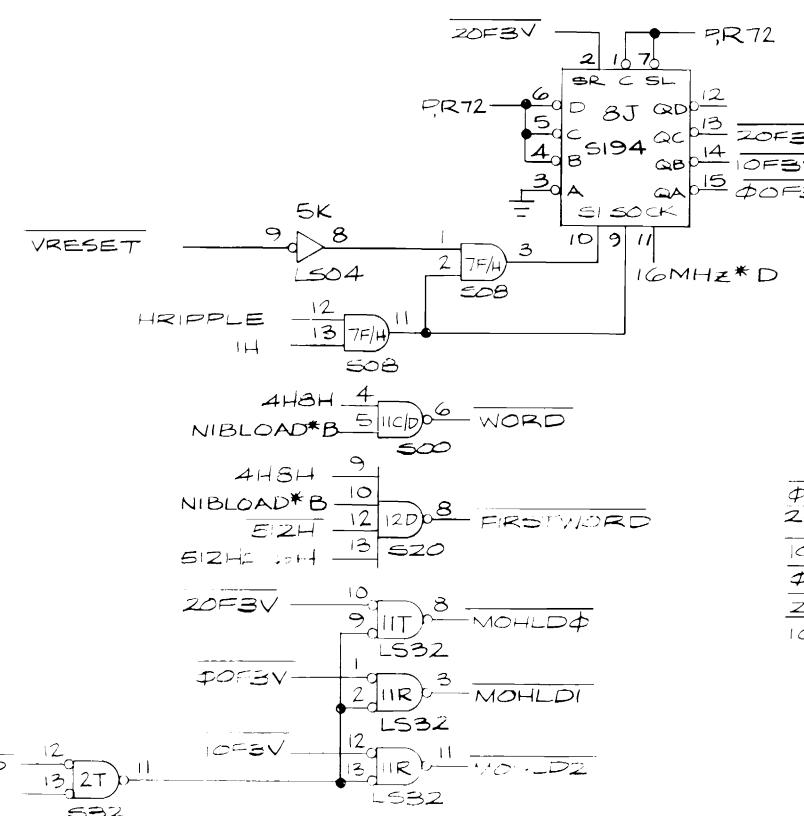
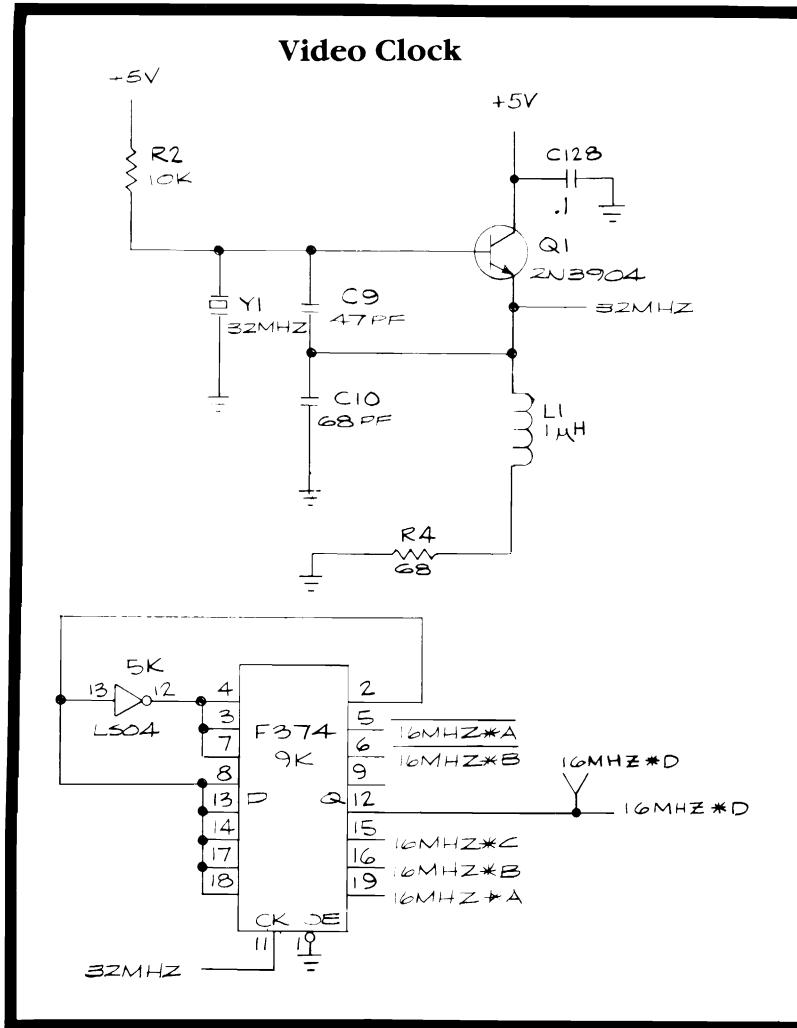


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**Paperboy™ Video PCB Schematic Diagram**

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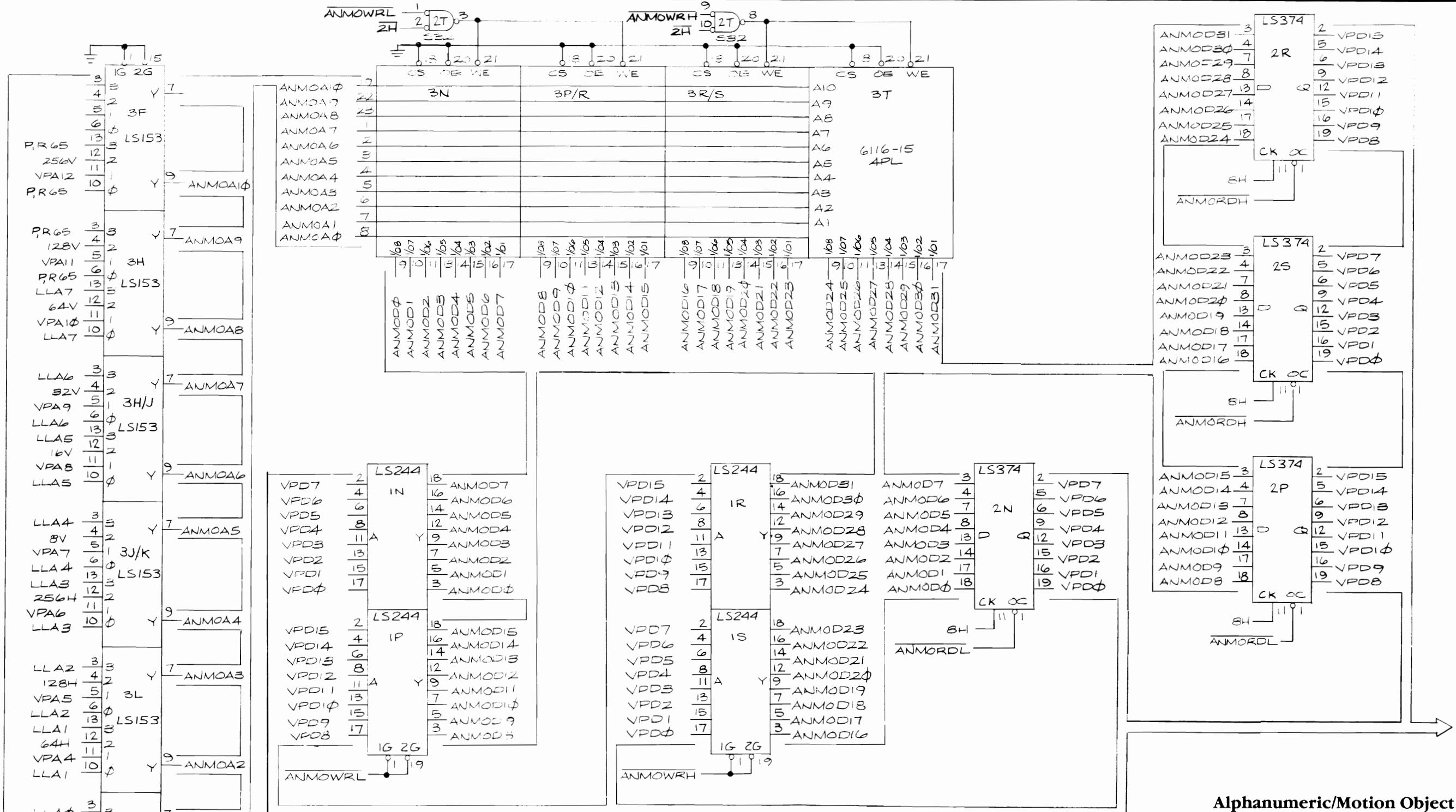


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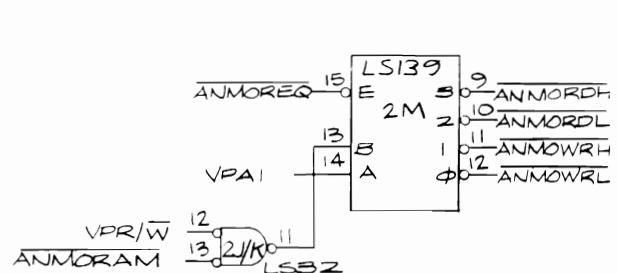
# Paperboy™ Video PCB Schematic Diagram





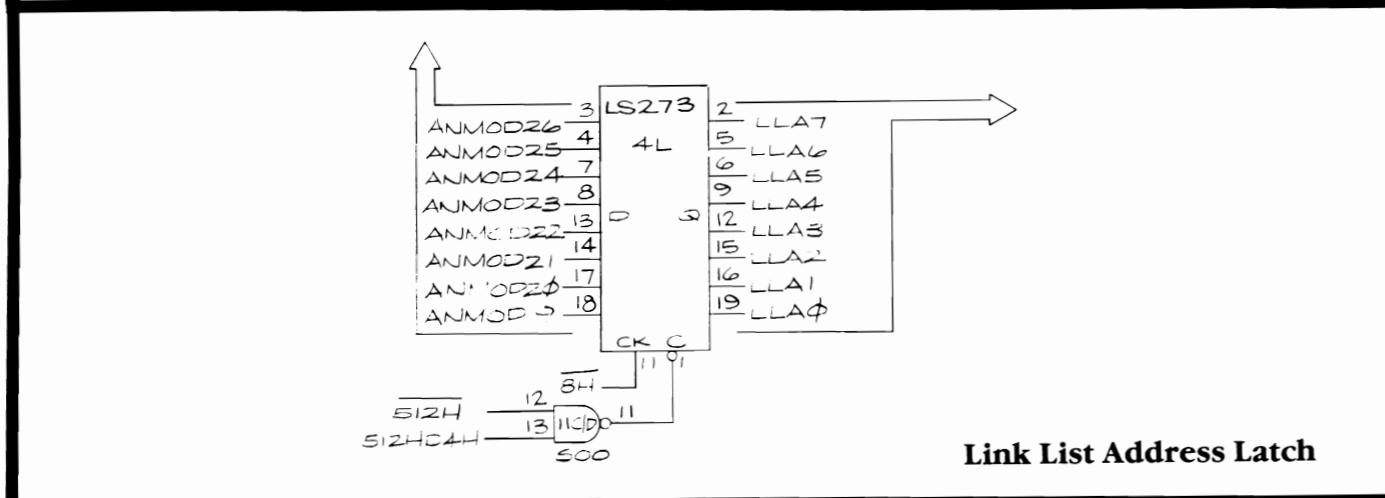
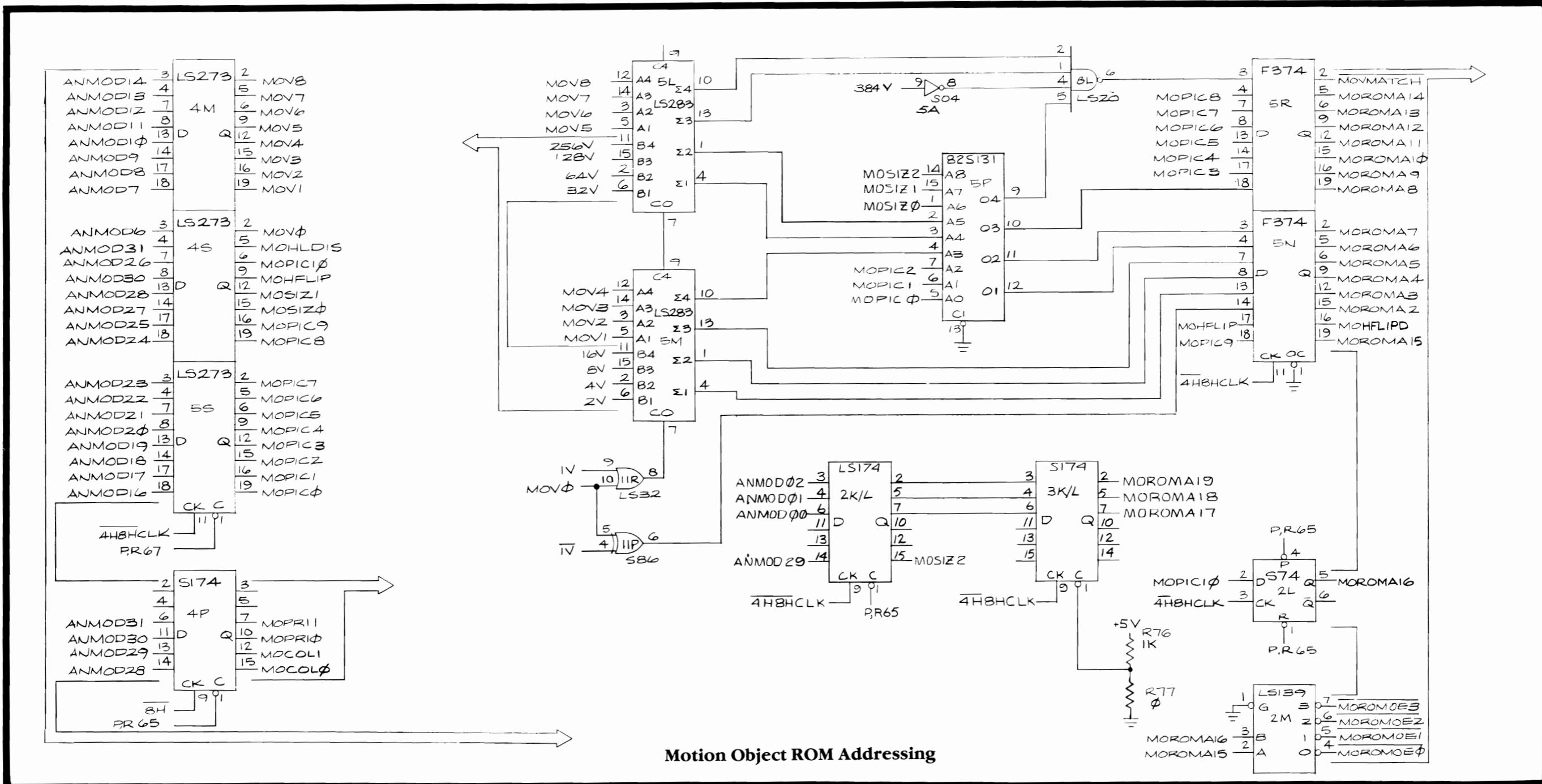
**Alphanumeric/Motion Object RAM**

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**Paperboy™ Video PCB Schematic Diagram**



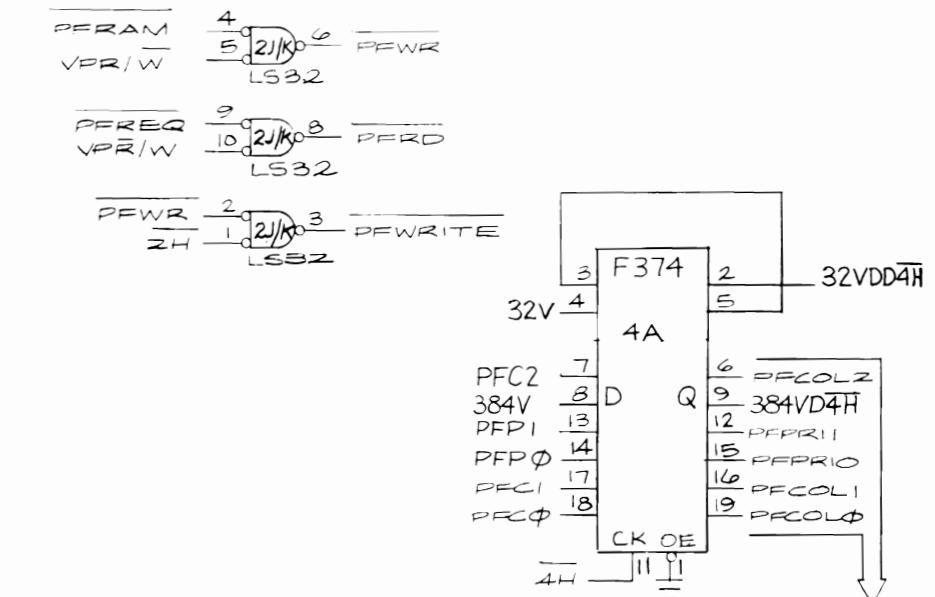
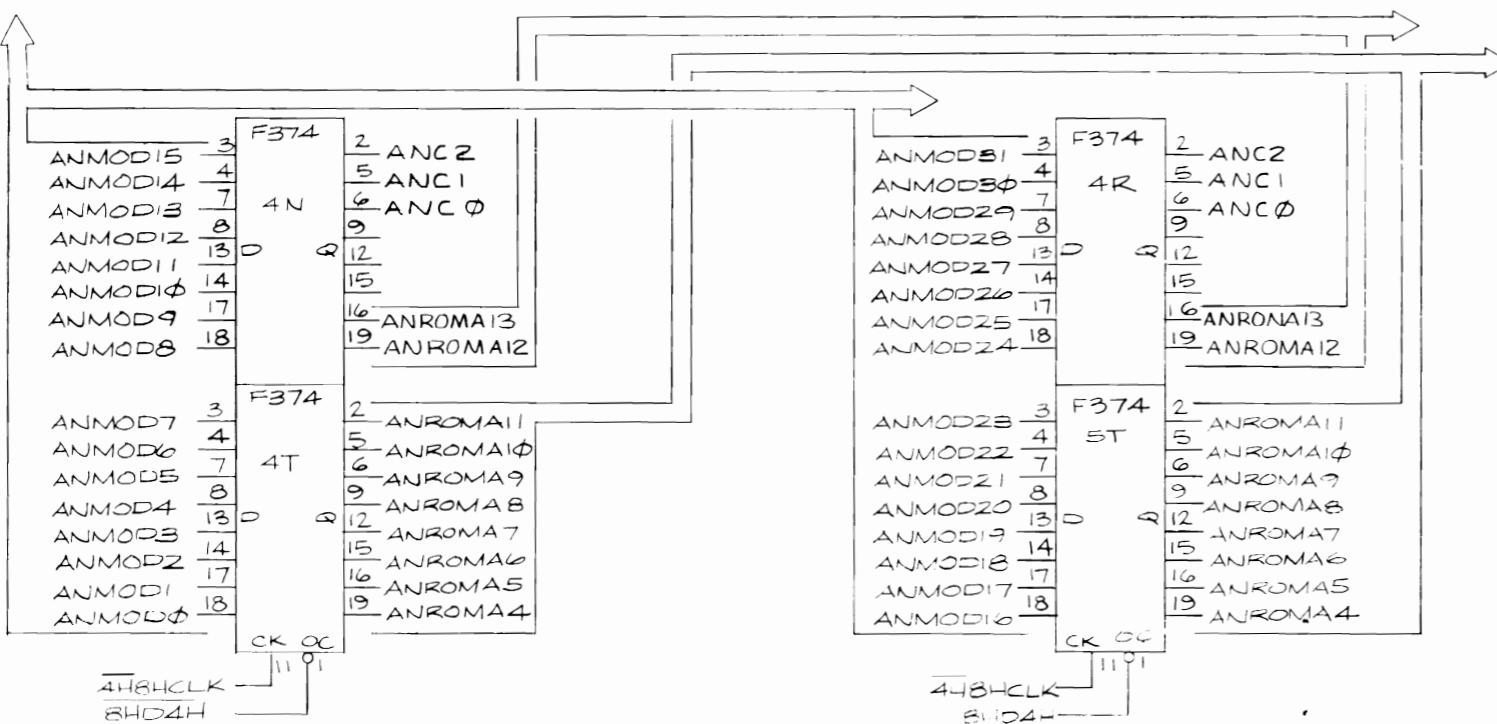


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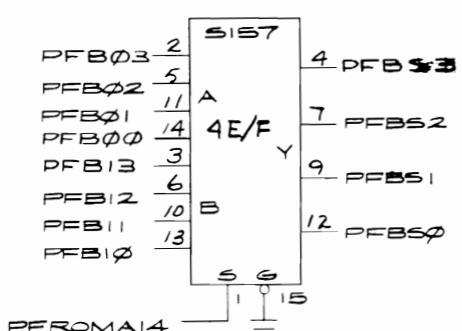
**Paperboy™ Video PCB Schematic Diagram**



### Alphanumeric ROM Addressing



Playfield Data Latch Strobes



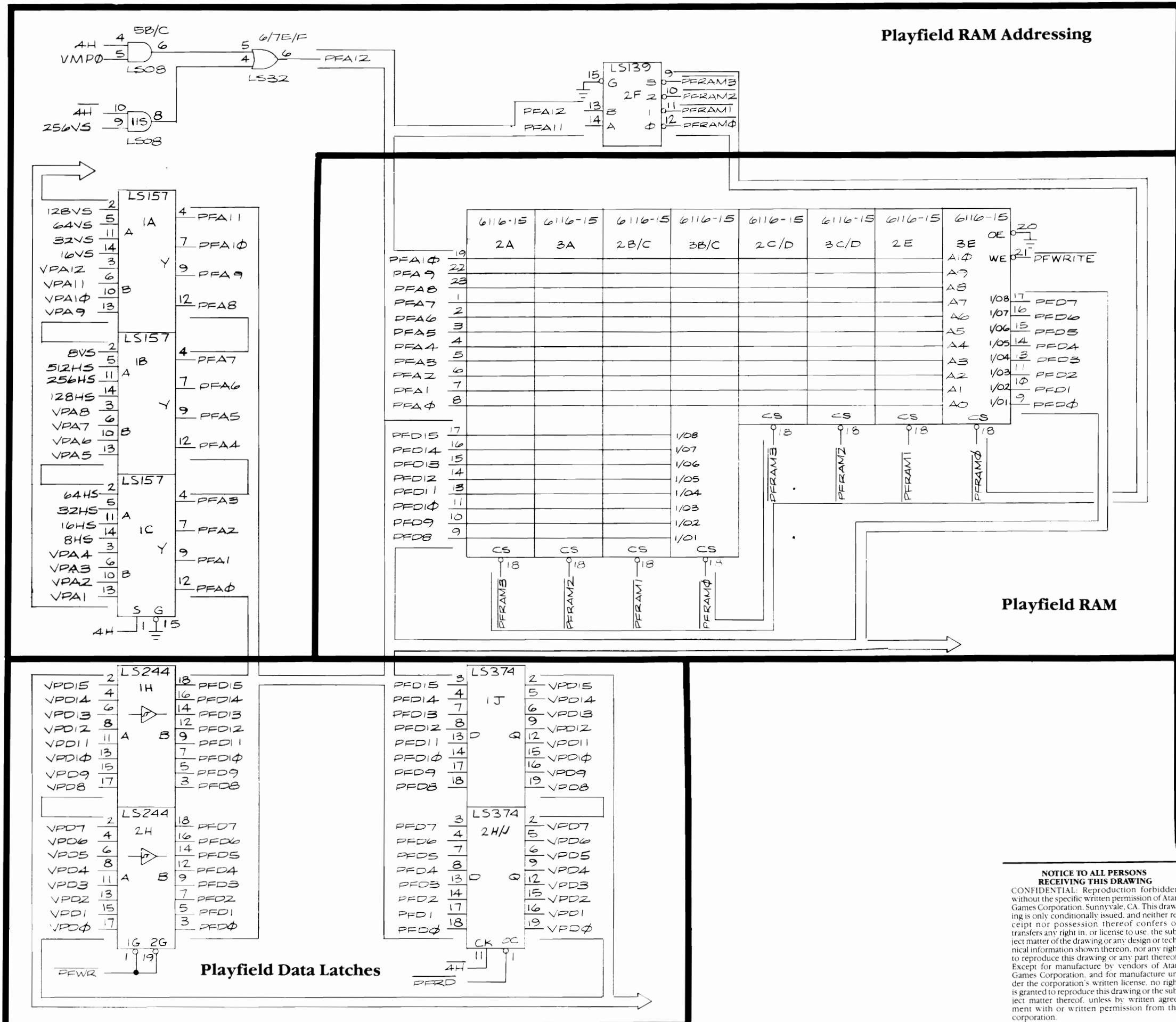
Playfield Bank Select

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**Paperboy™ Video PCB Schematic Diagram**



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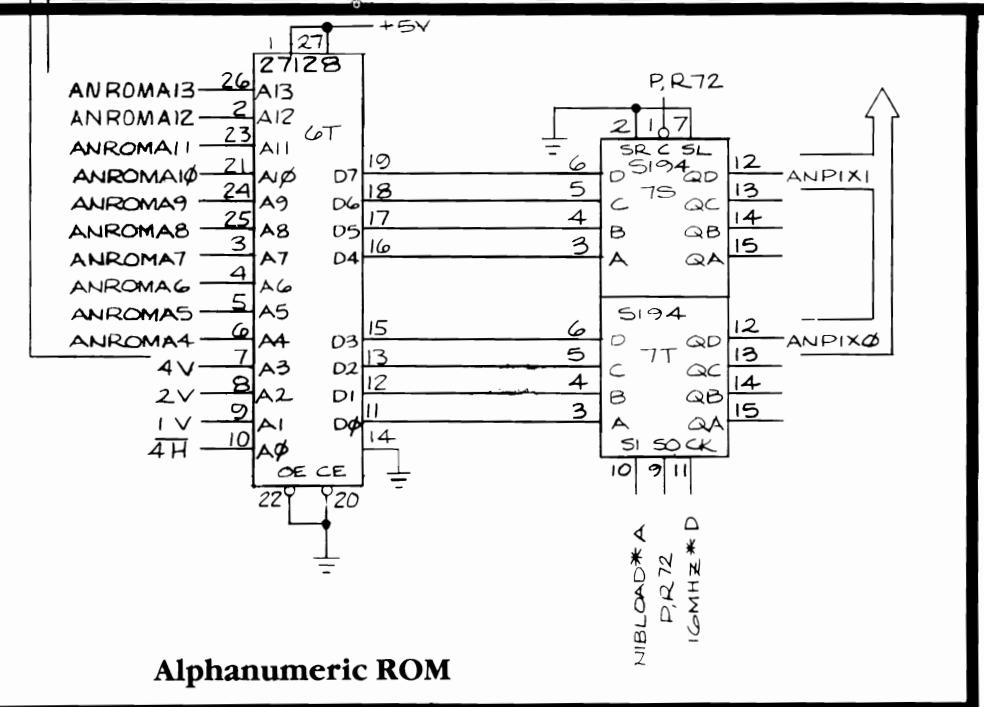
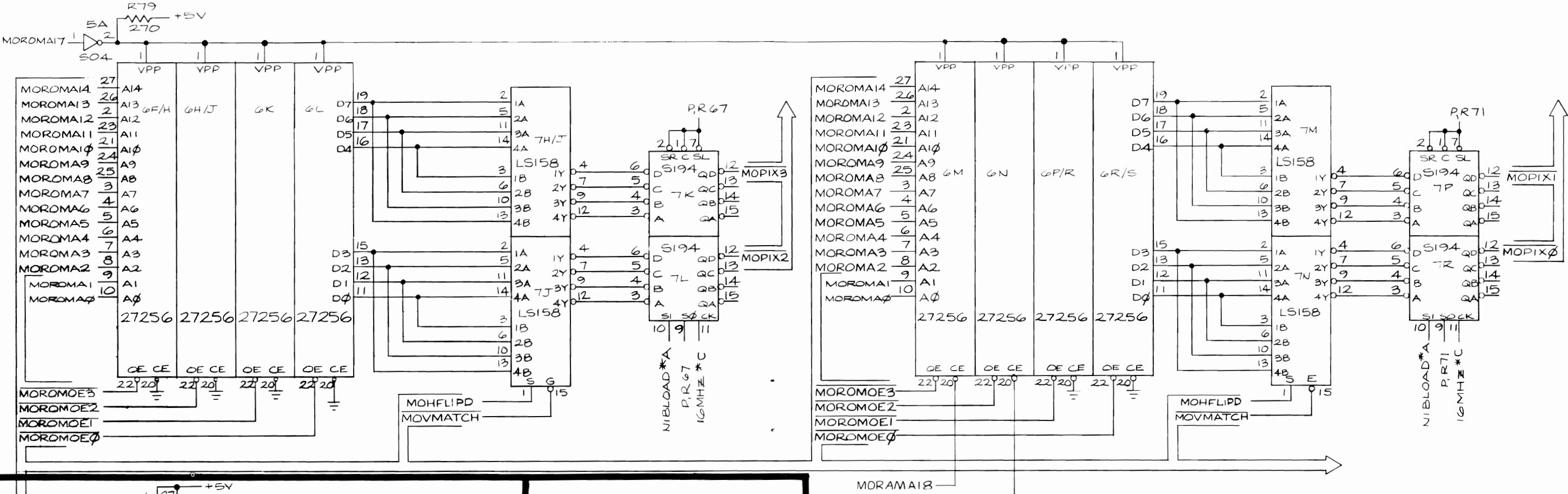
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**Paperboy™ Video PCB Schematic Diagram**

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### Motion Object ROM



**Alphanumeric ROM**

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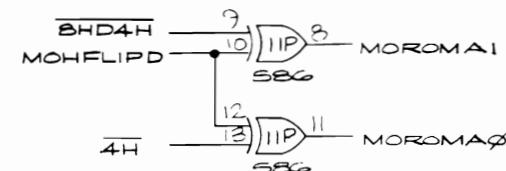
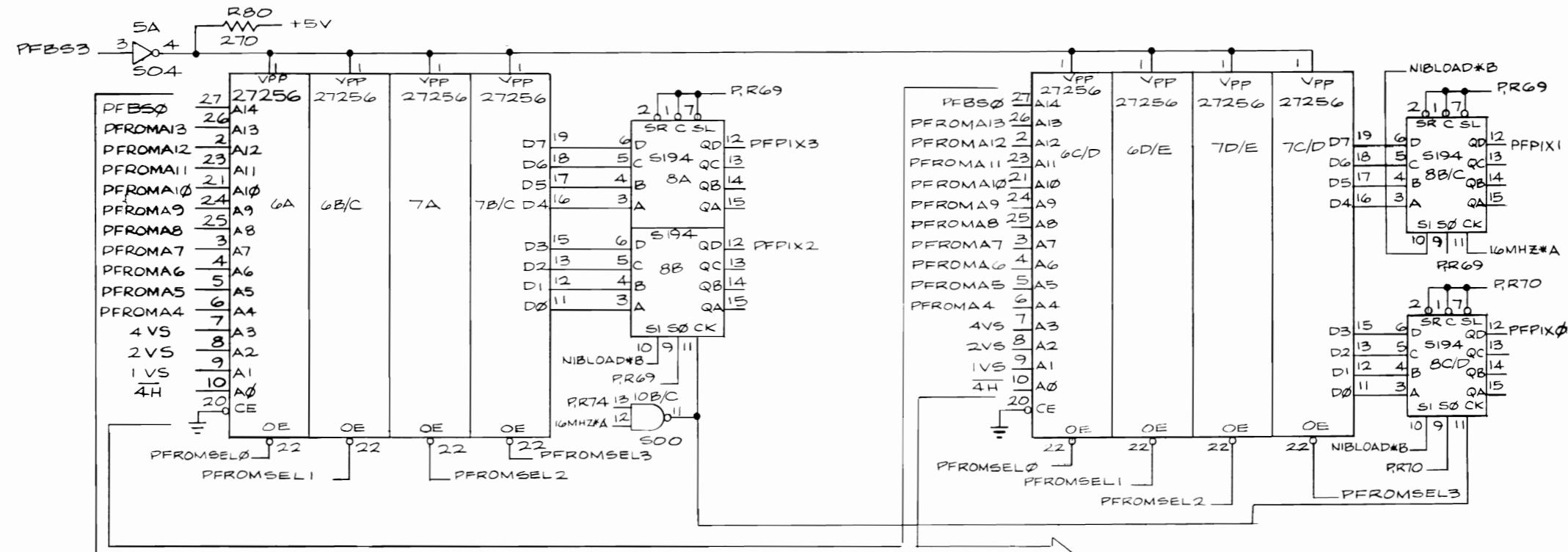
**Paperboy™ Video PCB  
Schematic Diagram**



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SP-275 Sheet 13B  
1st printing

### Playfield ROM



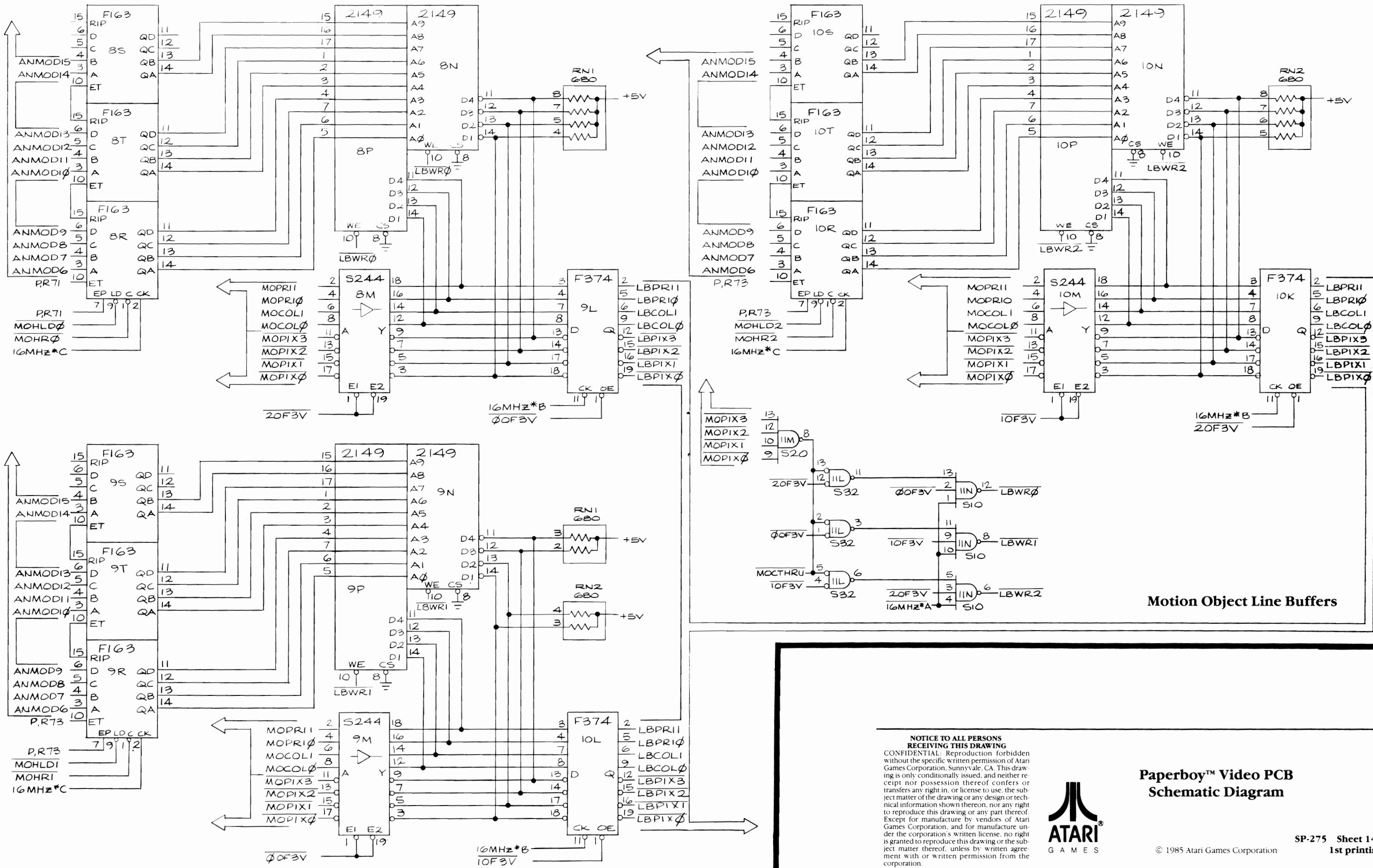
Motion Object ROM Addressing

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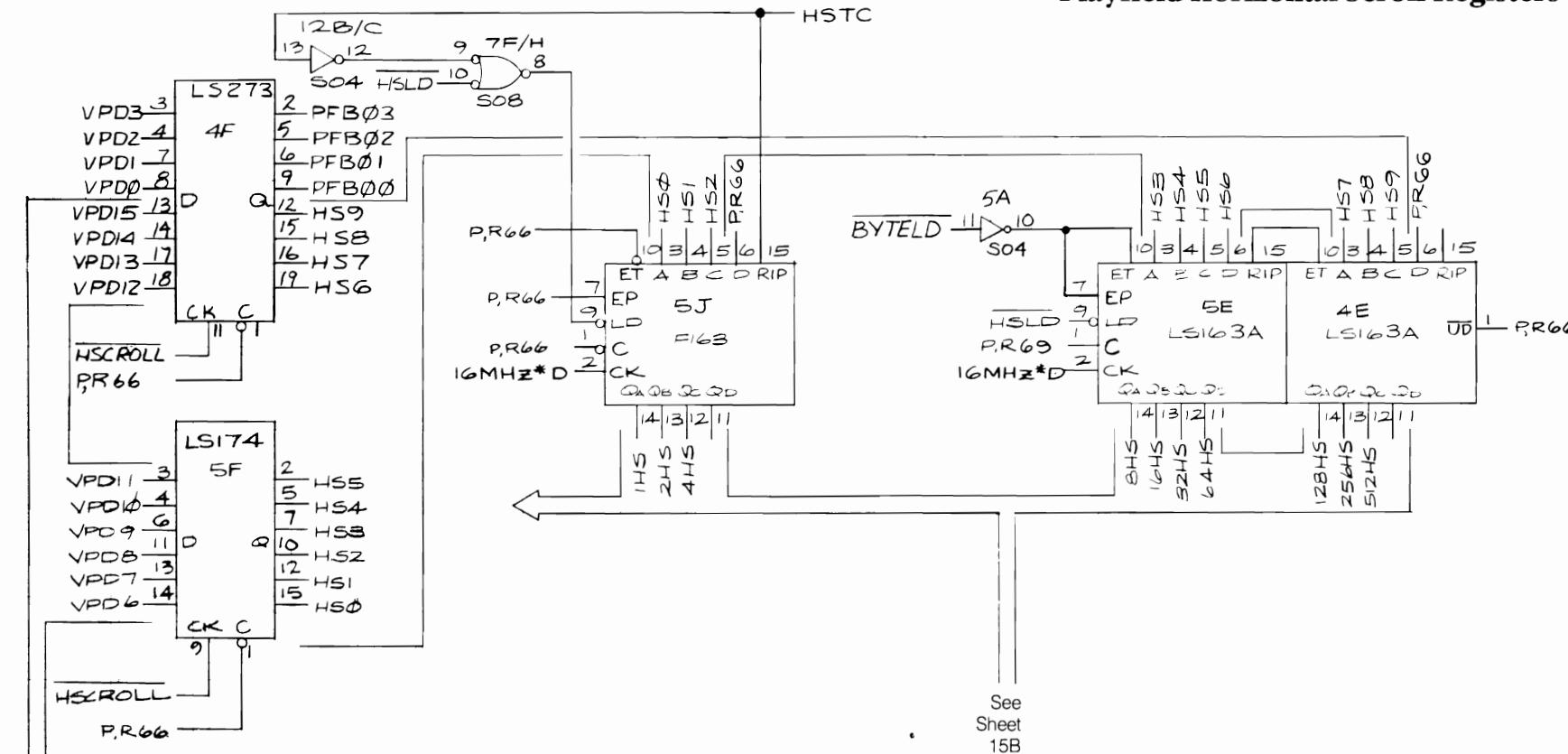
Paperboy™ Video PCB  
Schematic Diagram



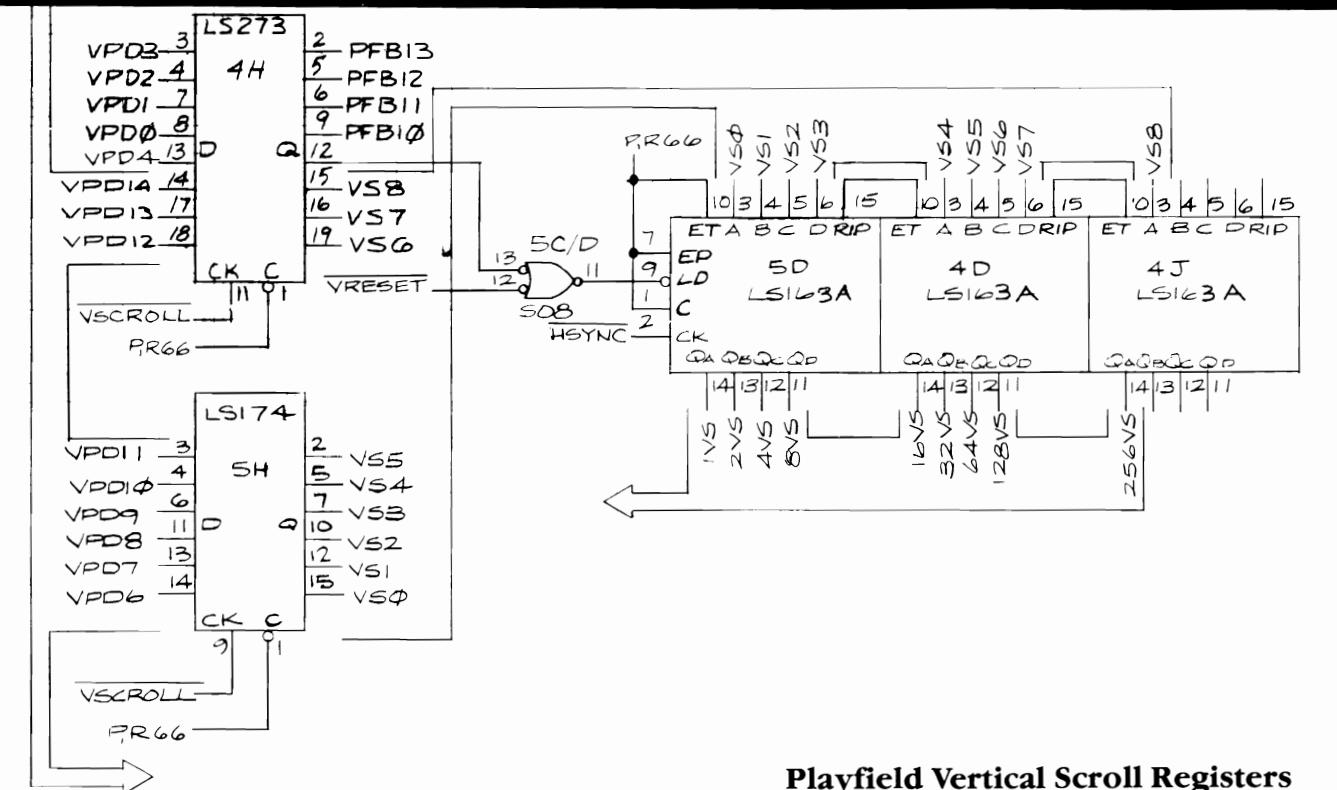
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### Playfield Horizontal Scroll Registers



### Playfield Vertical Scroll Registers

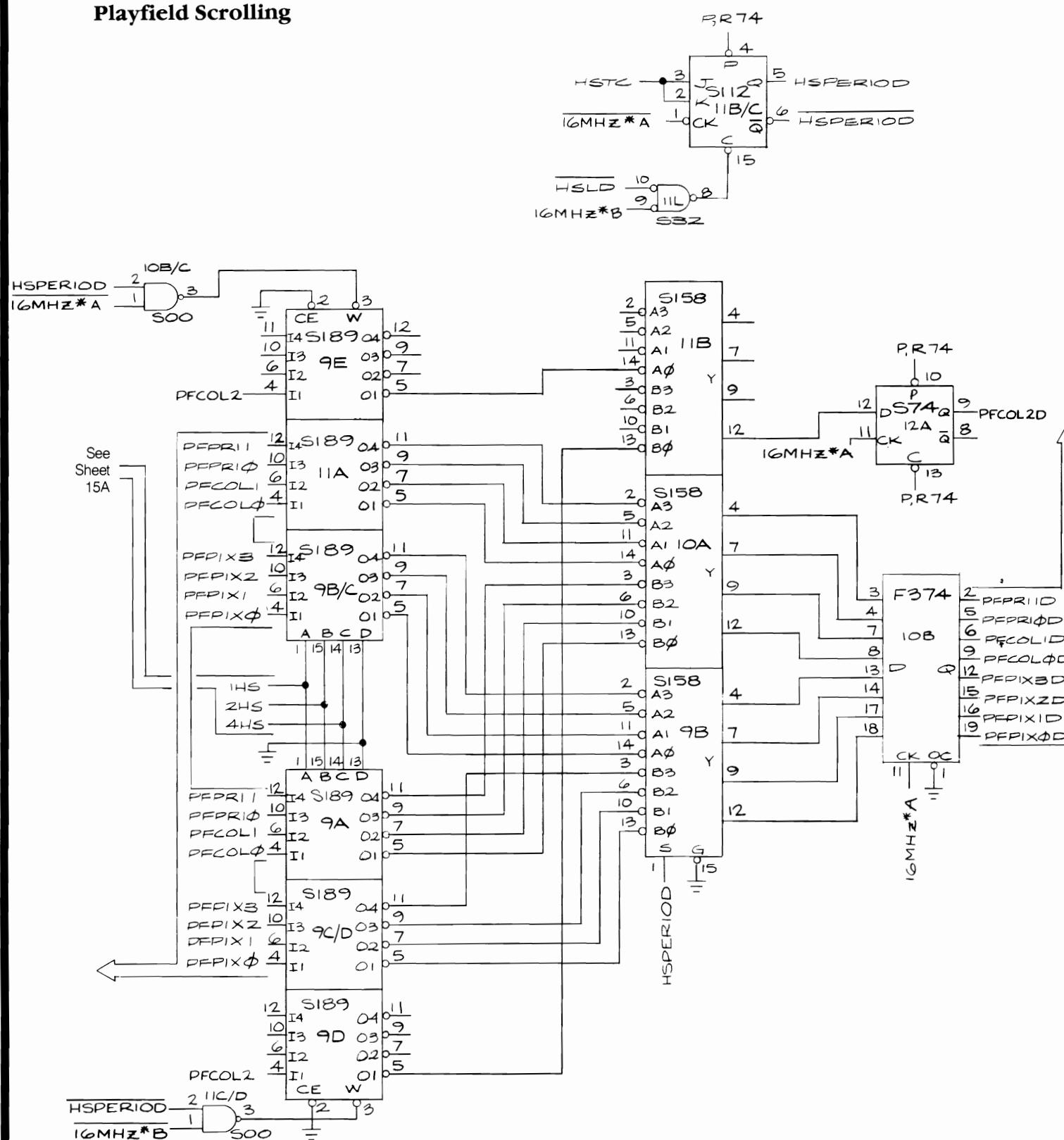


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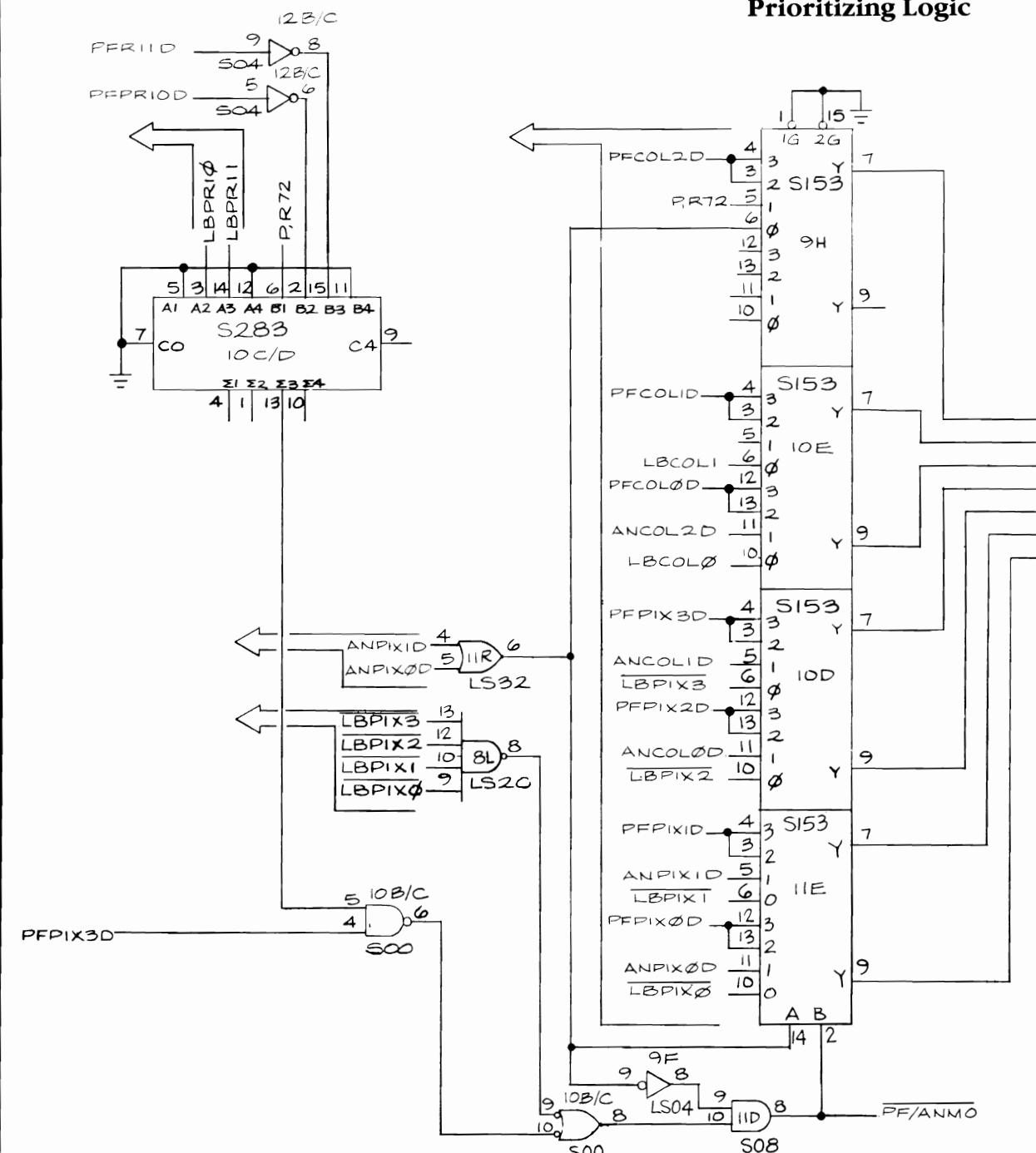
### Paperboy™ Video PCB Schematic Diagram



## Playfield Scrolling



## Prioritizing Logic

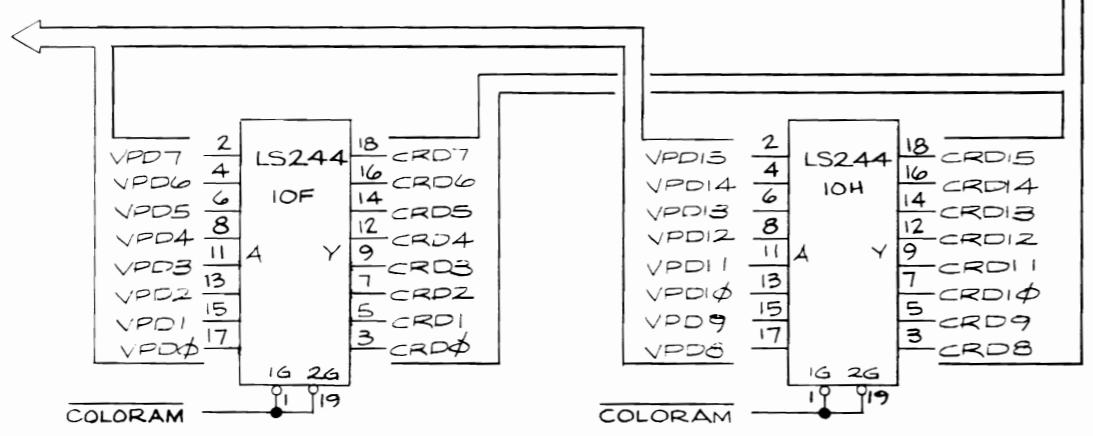
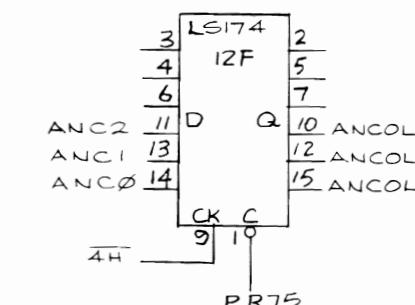
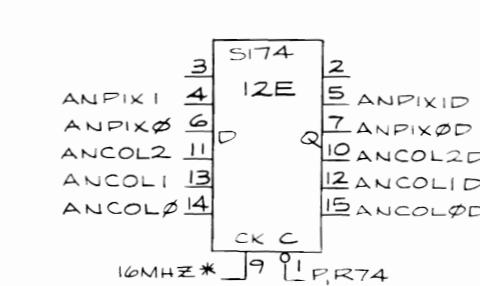
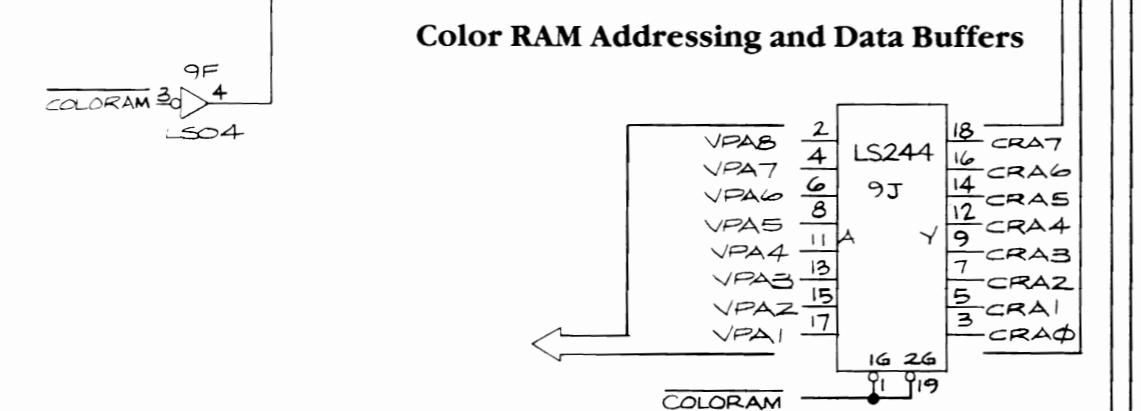
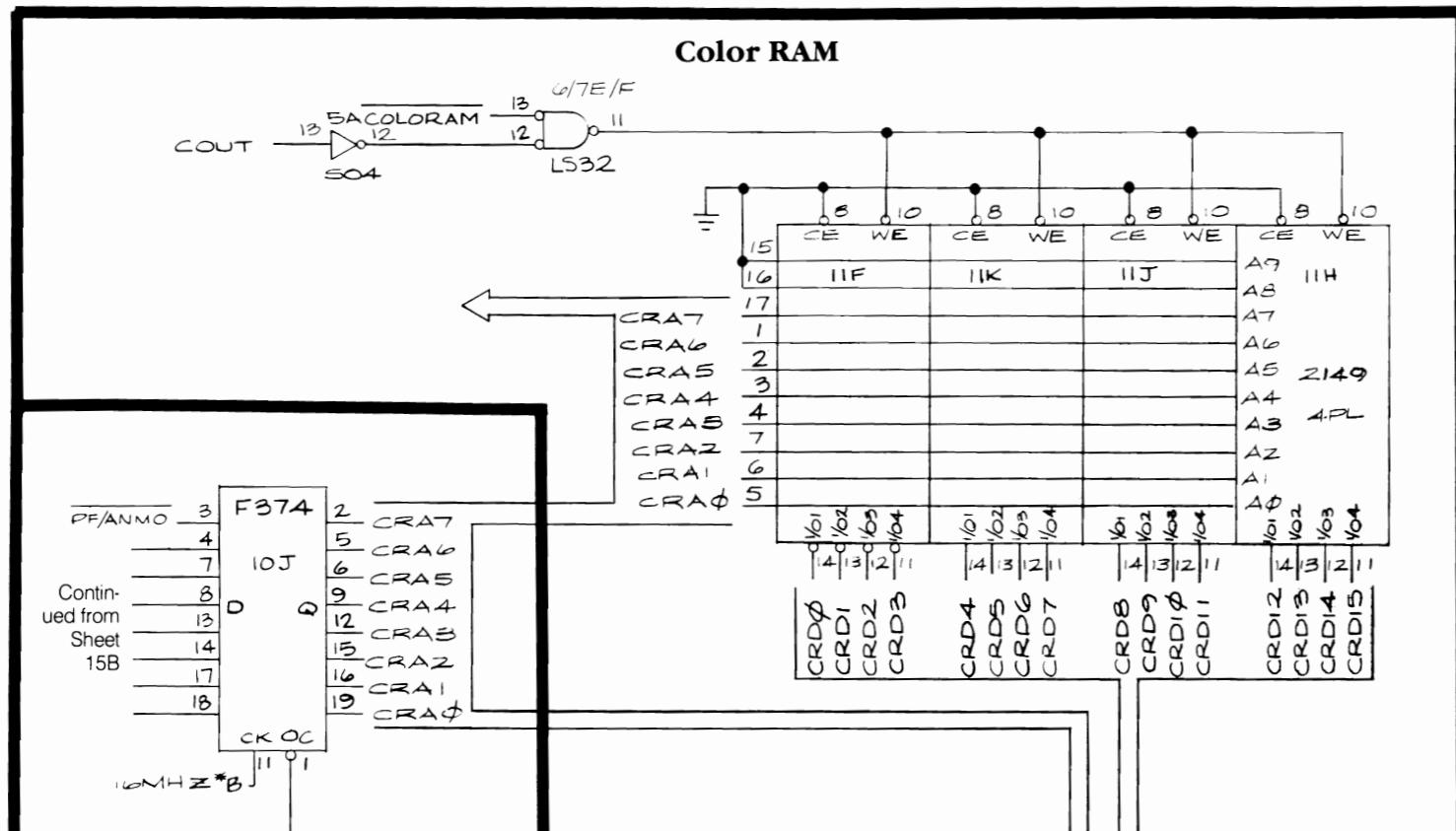


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# Paperboy™ Video PCB Schematic Diagram





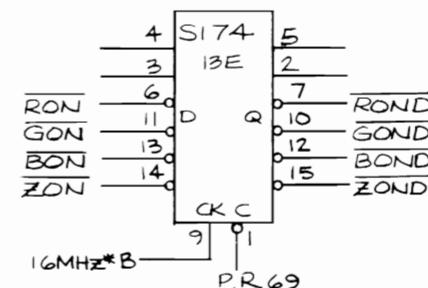
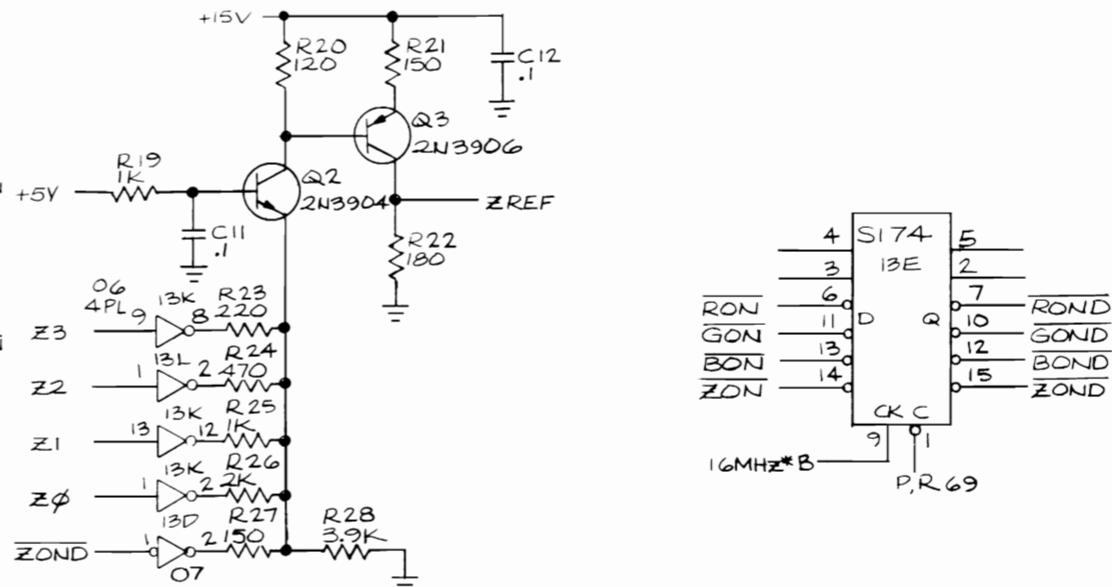
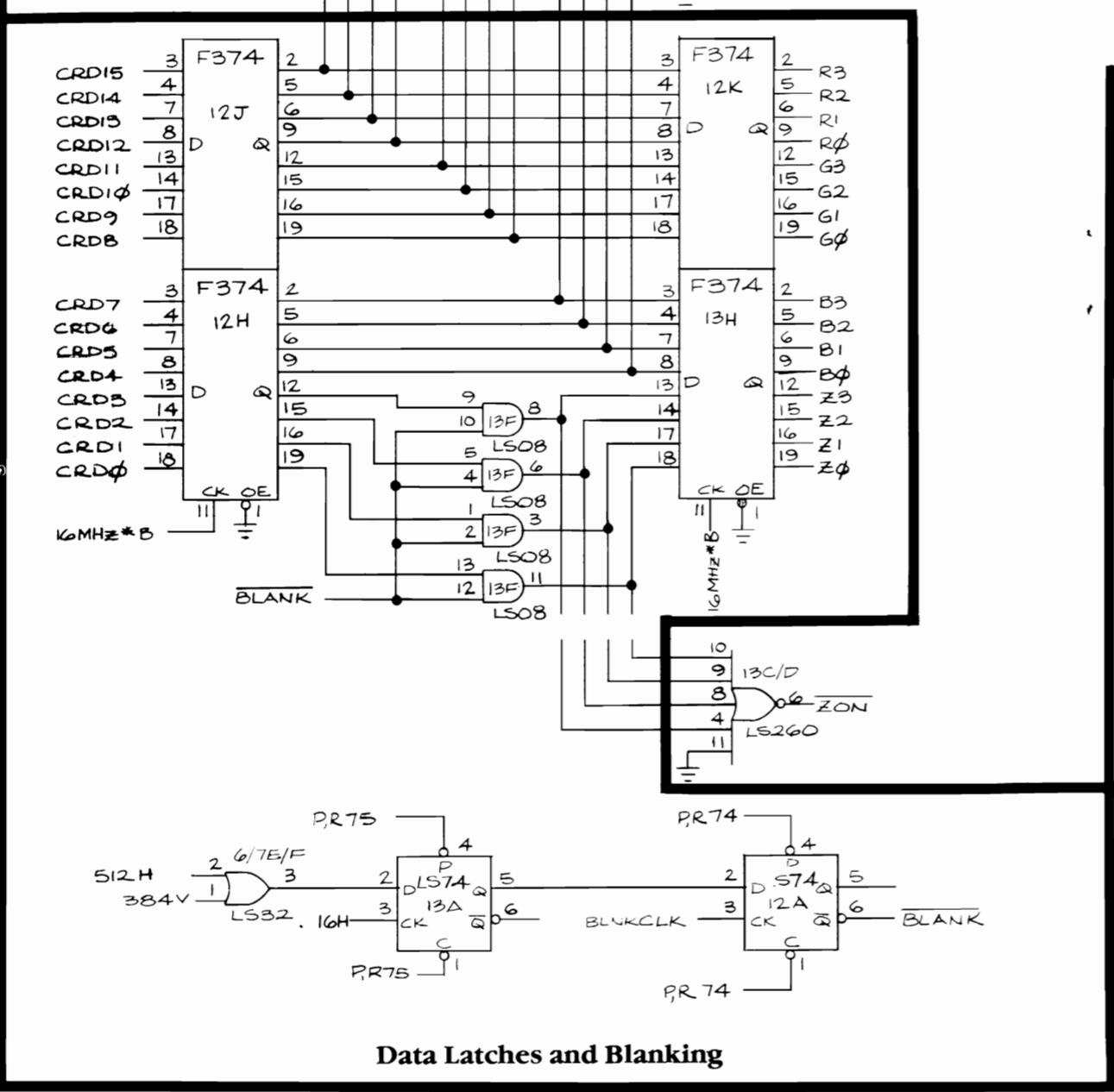
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**Paperboy™ Video PCB Schematic Diagram**

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## Video Intensity Control and Driver Enables



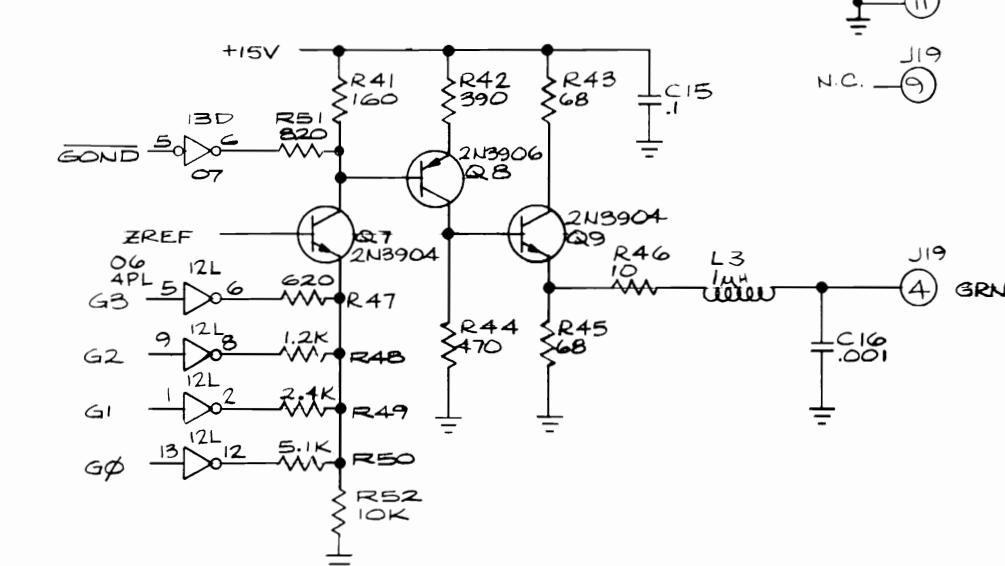
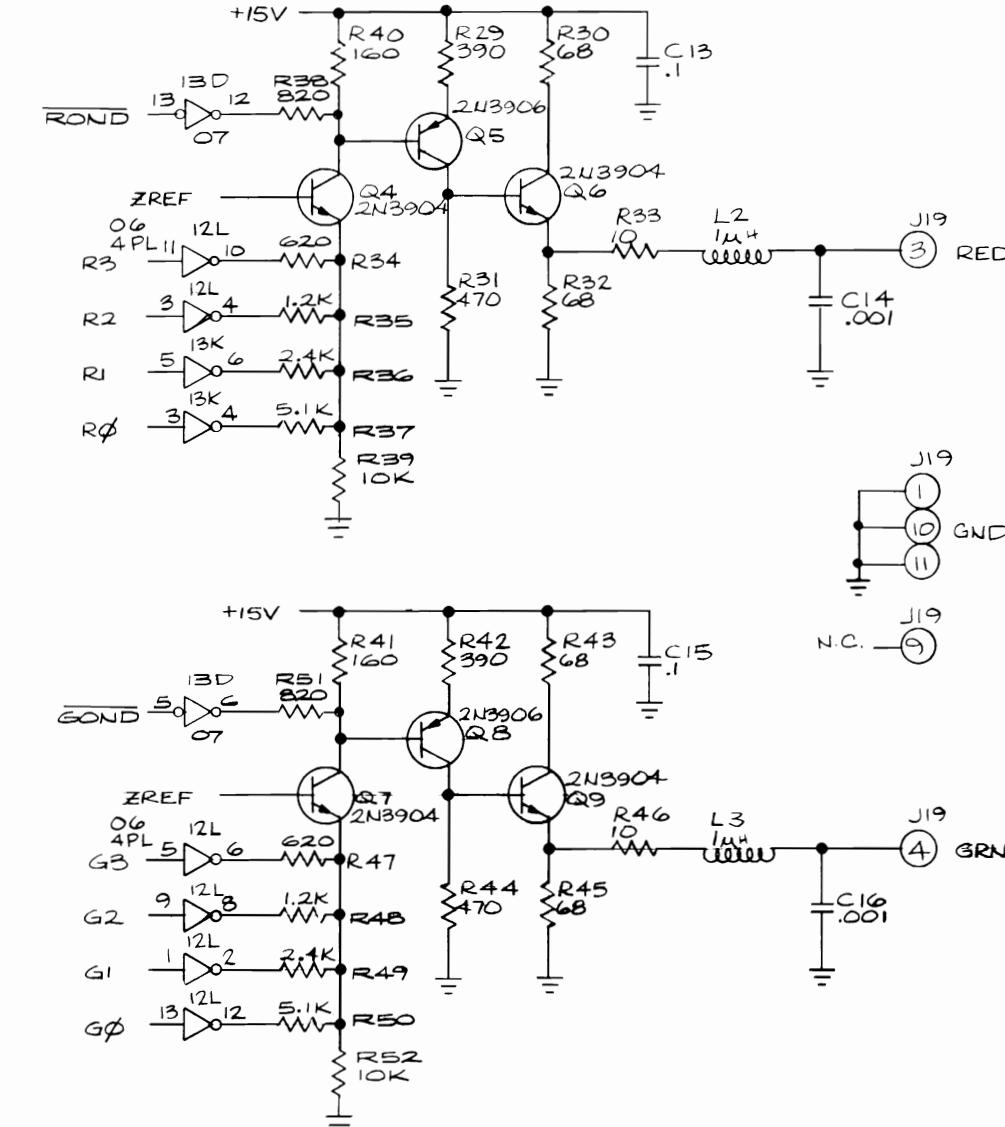
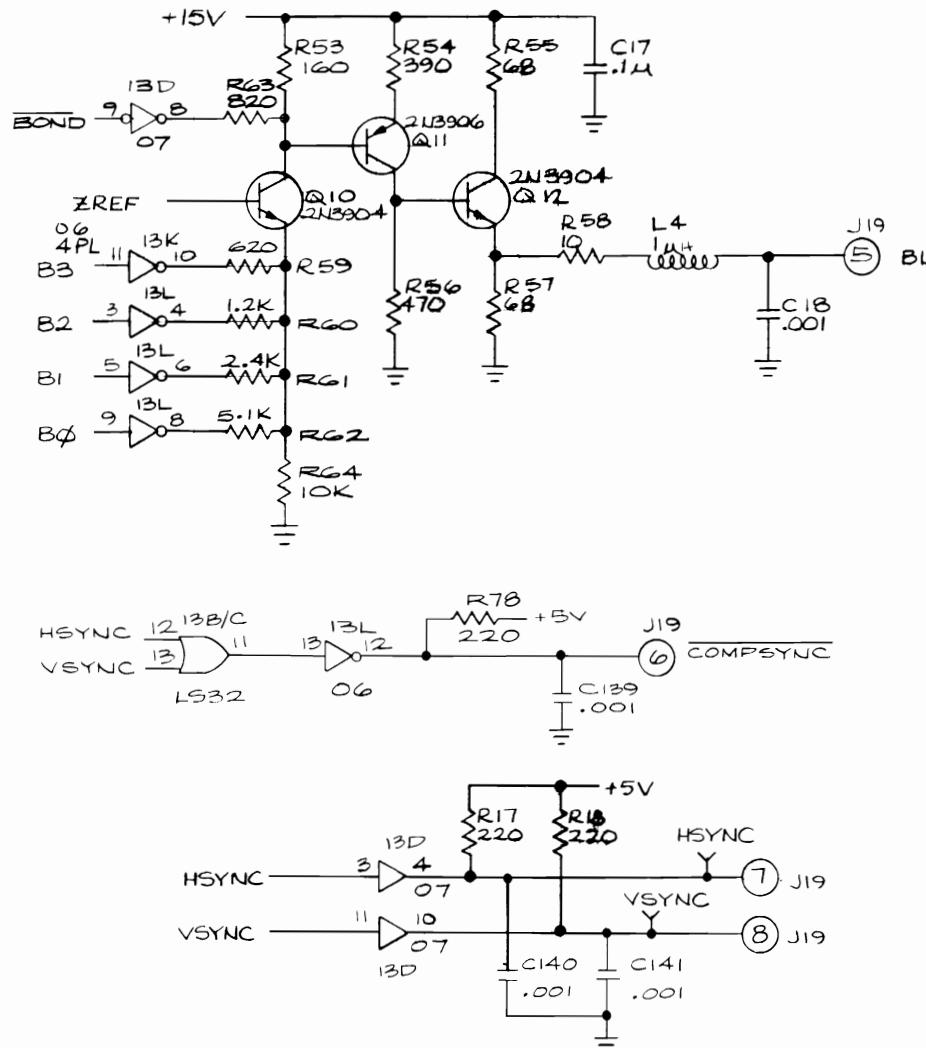
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**Paperboy™ Video PCB Schematic Diagram**



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## Output Drivers



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**Paperboy™ Video PCB Schematic Diagram**

